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Role of SiCl₄ addition in CH₃F/O₂ based chemistry for Si₃N₄ etching selectively to SiO₂, SiCO and Si

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Dry etching of amorphous silicon nitride (Si₃N₄) selectively toward silicon dioxide (SiO₂), silicon oxicarbide (SiCO) and crystalline silicon (c-Si) in an Inductive Coupled Plasma (ICP) reactor using CHF₃/O₂/He chemistry with SiCl₄ addition is studied. Plasma exposure of c-Si, SiO₂ and SiCO leads to an oxifluoride deposition. The deposition rate is the same for all these materials and increases linearly with the amount of SiCl₄ added. On the other hand, Si₃N₄ etching is observed at very small amount of SiCl₄ added (2 sccm) while oxide deposition takes place at higher SiCl₄ flow (10 sccm). Quasi-*in situ* Angle Resolved X-ray Photoelectron Spectroscopy (ARXPS) investigation shows oxifluoride SiO_xF_y deposition on c-Si and SiCO while a thin F-rich reactive layer is observed on Si₃N₄. The oxidation of Si₃N₄ surface with O2 plasma prior to CHF₃/O₂/He with small SiCl₄ addition plasma treatment showed that oxidation state plays a significant role on the etching / deposition equilibrium. In addition, it is found that oxifluoride deposition on Si_3N_4 is driven by ion energy, with deposition observed at 0 V substrate bias voltage while etching is observed for values higher than to 150 V. All these results show that a competition takes place between silicon oxifluoride deposition and etching, depending on substrate material, surface oxidation and ion energy. Based on additional Optical Emission Spectroscopy (OES) data, we proposed insights to explain the different etching and deposition behaviors



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observed. It is focused on the crucial role of ion energy and the nitrogen presence in Si_3N_4 leading to formation of CN and HCN, helping to get a thinner reactive layer and to evacuate etch by-products on Si_3N_4 while an oxifluoride build-up on the other materials takes place. This is the author's peer reviewed, accepted manuscript. However, the online version of record will be different from this version once it has been copyedited and typeset.

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I. INTRODUCTION

The downscaling of Complementary Metal-Oxide-Semiconductor (CMOS) devices induces the need of significant process development at each new technology node in order to meet the aggressive specifications for 2D architectures on Fully Depleted Silicon On Insulator (FDSOI) and for 3D architectures as Fin Field Effect Transistor $(FinFET)^1$ or stacked nanowires $(SNW)^2$. Independently of these transistor architectures, gate spacers have common interest. Their primary function is to electrically isolate the gate from the source and the drain and to define the effective channel length of the transistor by controlling the dopant profile³. Consequently, the spacer realization is critical regarding device performances. Gate spacers are commonly made of Si₃N₄⁴. It tends to evolve to materials with lower dielectrics constant such as SiCOH⁵ or SiBCN⁶ in order to reduce RC delay. However, Si₃N₄ spacers are still widely used at the lab or in the industry. Within the spacer module, the etching is particularly challenging for several reasons. First, spacers must be etched selectively toward the crystalline silicon (c-Si) constituting the source and the drain of the transistor and toward the silicon dioxide (SiO_2) of the shallow trench isolation and/or the Buried OXide (BOX) of FDSOI technologies. Indeed, c-Si damage and c-Si/SiO₂ recesses are not suitable regarding device integration. In addition, it is important for the subsequent implantation module to preserve spacer Critical Dimensions (CDs) and to have straight profiles without foot formation at the base of the spacer. Finally, a new challenge appeared with 3D architectures. Because the active is lifted for these integrations, the apparition of parasitic spacer on its sidewalls with conventional etch need to be addressed by increasing significantly the over-etch time⁷. High selectivity etching

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sequences are required to solve this specific issue in order to avoid significant recess into c-Si and SiO₂.

Plasma etching of Si₃N₄ for spacer application is mainly performed in ICP reactors using fluorocarbon C_xF_y or hydrofluorocarbon $C_xH_yF_z$, like CF₄, CHF₃ and CH₃F⁸. The Si₃N₄/c-Si etch selectivity is increased by combining fluorocarbons with gases enhancing polymerization, such as dihydrogen (H₂)^{9,10} and methane (CH₄)¹¹. By reducing the fluorine radical concentration in the plasma through HF formation¹², adding such gases leads to the formation of a carbon rich passivation layer $C_xF_y^{10}$ preferentially on c-Si. However, this gas mixture might induce the formation of a large foot¹³, leading to a shift in dopant profile which could degrade CMOS transistor electrical performances. In addition, carbon-rich plasmas can also induce damages to the source and the drain by carbon implantation¹⁴. Dioxygen (O₂) is usually added to these chemistries in order to have a precise control of the passivation layer thickness and to induce c-Si oxidation when landing on the active, enhancing selectivity⁸. Despite these optimizations, such etch processes do not meet all the requirements in terms of selectivity and profile control for 3D architectures spacer's, especially regarding the parasitic spacer removal mentioned above⁷.

Recent studies demonstrated the interest of adding silicon tetrachloride (SiCl₄) to $CH_3F/O_2/He$ chemistry¹³. Highly diluted SiCl₄ combined with significant O₂ flow induces a selective passivation layer deposition of SiO_xF_y over c-Si and SiO₂ while still etching Si₃N₄. Consequently, etch selectivity between these materials is significantly increased by SiCl₄ addition. As there is a significant SiO_xF_y deposition when landing on c-Si of the active, some oxide accumulation might occur around the active corner and prevent parasitic spacer etch removal on 3D architectures. Thus, the necessity to cycle the SiCl₄-based etch

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process with a carefully designed oxide etch step in order to overcome this issue has been demonstrated recently⁷, followed by process development and optimization of this etching sequence¹⁵. However, mechanisms of SiO_xF_y selective deposition on c-Si and SiO_2 while Si_3N_4 is etched remain unclear. The dependence of the initial growth kinetic on surface oxidation degree and thickness should be finely studied to take the full advantages of cyclic etch approaches.

In this paper, we first discuss the etching and deposition results on Si₃N₄, Si, SiO₂ and SiCO as a function of SiCl₄ flow. Reactive layer compositions are characterized by quasi *in situ* X-ray Photoelectron Spectroscopy (XPS) to clarify their compositions and thicknesses evolution. Then, Si₃N₄ oxidation before etch and bias voltage are studied in order to highlight surface oxidation impact on etching and deposition mechanisms. These results are discussed to explain observed selectivity. Finally, OES signals are analyzed to highlight etching and deposition mechanisms.

II. EXPERIMENTAL SETUP

A. Materials

Thin films used in this study are deposited on 300 mm silicon blanket wafers or 16 nm thick Silicon On Insulator (SOI) wafers with 145 nm buried oxide. Si₃N₄ with thicknesses of 53 or 140 nm are deposited by Low Pressure Chemical Vapor Deposition (LPCVD) at 750 °C from SiH₄ and NH₃ precursors. Thermally grown SiO₂ and SiCO deposited by PEALD at 400 °C, both 100 nm thick, are also used.

B. Process conditions

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Etching or deposition experiments are conducted on 300 mm ICP Kiyo FX etch module from LamResearch. Wafers are electrostatically clamped and chuck temperature is regulated thanks to backside He flow at 50 °C. Both source and chuck RF generators are operating at 13.56 MHz in continuous mode. All experiments in this study are conducted in bias voltage mode with continuous adjustment of bias RF power. CH₃F/O₂/He (200/200/120 sccm) with SiCl₄ addition ranging from 0 to 10 sccm gas mixtures are studied. Unless otherwise mentioned, plasma process conditions are 400 W for source power, 90 mtorr of total pressure, 60 s of exposure time and 250 V bias. Some experiments are conducted after thin films exposure to O₂/He (200/220 sccm) plasma with 1000 W for source power, 10 mtorr of total pressure, 20 s of exposure time with 100 V or no bias applied to the substrate. Between experiments, the etch chamber is systematically cleaned and pre-coated in SiCl₄/O₂ chemistry. Wet etching in hydrofluorydric acid (HF) diluted to 1% in deionized water during 30 s is used prior (if mentionned) or after plasma exposure to remove native or deposited oxide, respectively.

C. Characterization setup

Films thicknesses are characterized by ex-situ spectroscopic ellipsometry with wavelength ranging from 400 to 1000 nm on Atlas III from Nanometrics. All models include an oxide surface layer defined as a transparent film with thermal SiO₂ optical properties. This layer is used to describe native or plasma deposited oxide layers. Only films thicknesses are adjusted to experimental data.

Surface composition are carried out by quasi-*in situ* Angle Resolved X-ray Photoelectron Spectroscopy (ARXPS) measurements on a Thermo Fisher Scientific[®] Spectroscopy Theta 300^{TM} tool operating with a monochromatic Al K α x-ray source at

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1486 eV. Wafers are transferred from etch to XPS chamber via a specific 300 mm compatible vacuum (pressure $< 1 \times 10^{-4}$ mbar) carrier preventing from oxidation and airborne contamination^{16,17}. Survey and narrow spectra are recorded with a pass energy of 100 eV and an energy step of 0.5 and 0.1 eV, respectively. A spot size of 400 µm and 8 measurement angles, noted θ in the following, from 23.75 to 76.25 ° with respect to the surface normal are acquired. To limit surface charging, a flood gun is used, and the binding energy scale is referred to the adventitious C1s peak at 284.5 eV.

Optical Emission Spectroscopy (OES) measurements are achieved through OES plus setup provided by LamResearch. It covers a wavelength range from 185 to 983 nm with 2.0 nm spectral resolution and 100 ms integration time. The table reported by Piet.et.al¹⁸ and references herein is used for the identification of H* and O* lines and CN*, N_2 * and NH* bands. Signal from He* at 587.7 nm is used to monitor the plasma electron density.

III. RESULTS AND DISCUSSION

A. Etch and deposition kinetics on Si₃N₄, Si, SiO₂ and SiCO

Figure 1 shows the influence of SiCl₄ flow on oxide deposition rate on Si₃N₄, Si, SiO₂ and SiCO. Deposition rate are calculated from linear regression of deposition thickness versus plasma exposure time from 10 to 300 s. Deposition rates on Si, SiO₂ and SiCO increase linearly with the increasing of SiCl₄ flow from 2 to 10 sccm. They appear to be independent of the substrate material and reach 0.45 ± 0.01 nm.s⁻¹ at 10 sccm of SiCl₄. In contrary, a threshold is observed on Si₃N₄ with no deposition at 2 sccm and deposition rate up to 0.36 nm.s⁻¹ at 10 sccm of SiCl₄. In such condition, deposition rates remain 25 %

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faster on Si, SiO₂ and SiCO than on Si₃N₄. It suggests that a competition between etch and deposition occurs on Si₃N₄. In the following, we focus on highly diluted SiCl₄ conditions with 2 sccm inlet flow, which presents a more contrasted sensitivity to materials. Kinetics of Si₃N₄, Si and SiCO films consumption are plotted on Figure 2 for plasma exposure from 10 to 300 s. Wet treatment by HF 1% during 30 s is applied before measurement in order to remove the deposited layer.



Figure 1: Dependence on SiCl₄ flow rate of overlayer deposition rate on Si₃N₄, Si, SiO₂ and SiCO.



Figure 2: Si₃N₄ (left axis), Si, and SiCO (right axis) consumption versus plasma exposure time for 2 sccm SiCl₄ flow rate. Measurements after HF 1% 30s treatment are presented by open symbols.

Si₃N₄ consumption increases linearly with time, with an etch rate of 0.31 nm.s⁻¹. The HF treatment leads to a constant increase of Si₃N₄ consumption by a mean value of 1.6 nm. In contrast, Si and SiCO consumptions after plasma processes and HF treatment appear to be independent of plasma exposure time with values remaining below 1.0 and 2.2 nm on Si and SiCO, respectively. First, these observations show that HF 1% wet treatment efficiently removes the deposited silicon oxide at a rate higher than 0.66 nm.s⁻¹. Second, it underlines that strongly diluted SiCl₄ conditions could lead to selective deposition on Si, SiO₂ and SiCO while Si₃N₄ is etched. Finally, the 1.6 nm mean consumption induced by HF wet treatment post plasma exposure gives a first upper limit approximation of the reactive layer thickness.

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To get insights into mechanisms and to figure out why deposition takes place on Si and SiCO films while Si₃N₄ is etched, surface analysis after plasma exposure are performed by quasi *in-situ* XPS and compared to measurements on pristine films. Figure 3 presents Si2p high resolution spectra acquired at 23.75° relative to surface normal take-off angle on pristine SiCO, Si, and Si₃N₄ and after plasma exposure of 30 and 60 s. After 30 s of plasma exposure, a peak at 104.0±0.5 eV attributed to Si-O bonds¹⁹ appears on SiCO and Si while peaks at 102.7 and 99.2 eV attributed respectively to bulk Si-O-C²⁰ and Si-Si¹⁹ contributions intensities decrease. This is even more pronounced after 60 s of plasma exposure. The attenuation of signals attributed to Si or SiCO bulk materials confirms the growth of a film containing silicon oxide on its top. At the opposite, on Si₃N₄, the ratio between peaks at 101.8 eV attributed to Si-N bonds²¹ and at 103.5 eV attributed to Si-O bonds is not affected by plasma exposure. Si-N contribution dominates Si2p spectra independent of plasma exposure time. It suggests a steady etching of Si₃N₄ film through the formation of a thin reactive surface layer, in accordance with kinetics results previously presented on Figure 2.



Figure 3 : Si2p high resolution spectra at 23.75° relative to surface normal takeoff angle on Si₃N₄ (a), Si (b), and SiCO (c) after 30 and 60 s of plasma exposure with 2 sccm SiCl₂ flow rate and vacuum transfer to XPS analysis chamber. Pristine films are measured ex-situ.

In addition, surface compositions are reported in Table 1. After 30 s of plasma exposure, silicon oxifluoride films deposited on Si and SiCO present stoichiometries of SiO_{1,8}F_{0,9} and SiO_{2,6}F_{1,2}, respectively. The deposit on SiCO appears O richer than on Si. However, O1s signal measured on SiCO could be partially attributed to bulk SiCO and not only oxifluoride deposit. Stoichiometries extracted from grazing angle measurements (θ =76.25°), with higher surface sensitivity, lead to SiO_{1.8}F_{0.8} on Si and SiO_{1.9}F_{0.9} on SiCO.

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So, we can conclude that oxifluoride deposited on these films presents a similar composition. In contrast, plasma exposed Si₃N₄ surface, which could be characterized by the ratio between Si bonded to N and N bonded to Si, presents a ratio of 0.78, which is very close to the ratio of 0.77 measured on pristine material. Main differences on Si₃N₄ surface induced by the plasma exposure are the reduction of C and the presence of F-Si bonds. Surface contamination removal and reactive layer sensitivity allowed by quasi in-situ measurements could explain these observations.

Element (%)		Pristine films			After 30 s plasma exposure			After 70 s plasma exposure with O ₂ plasma pre-treatment With no With	
		Si ₃ N ₄	Si	SiCO	Si ₃ N ₄	Si	SiCO	bias Si ₃ N ₄	100Vb Si ₃ N ₄
	C-C	5.4	10.4	6.4	1.5	1.5	2.5	-	-
C1s	C-O	1.4	2.0	-	-	-	-	-	-
	C=O	-	1.2	-	-	-	-	-	-
	C-Si	-	-	12.3	-	-	1.6	-	-
Cl2p	Cl-Cl	-	-	-	1.3	0.6	-	-	-
F1s	F-Si	0.6	-	-	11.7	21.5	20.6	11.7	24.5
O1s	O-Si	17.5	32.0	47.0	11.8	42.7	45.8	14.2	43.8
N1s	N-Si	38.2	-	-	38.0	-	-	38.0	2.2
	Si-O	7.6	9.0	20.9	6.2	24.1	17.4	7.1	26.6
Si2p	Si-N	29.3	-	-	29.6	-	-	29.0	2.8
	Si-C	-	-	13.4	-	-	12.1		
	Si-Si	_	45.5	-	-	10.6	-		

Table 1 : Surface composition measured by quasi in-situ XPS ($\theta = 23.75^{\circ}$) on Si, SiCO, and Si₃N₄ before and after plasma exposure. Pristine films are measured ex-situ. Measurements with additional O₂ plasma pre-treatment are also reported.

Table 1 also reports few % of chlorine after plasma exposure on surfaces of Si_3N_4 and Si films. Chlorine is also observed on SiCO for shorter 10 s plasma exposure and on Si sample for longer 120 s experiments. These values, ranging from 0 to 1.3 %, are close to the detection limits of the experimental conditions used in this study. Figure 4 plots Cl2p

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and F1s surface compositions measured on Si₃N₄. Si, and SiCO samples as a function of plasma exposure time for a much more surface sensitive condition (θ =76.25°). On Si₃N₄ sample, chlorine continuously decreases from 3.5 % after 10 s of plasma exposure until 0 % after 240 s. Fluorine surface composition ranges between 20.5 and 22.4 % and appears independent of process duration. As suggested by Ullal et.al.^{22,23} and Kogelschatz et.al.²⁴, these observations could be interpreted as the replacement of Si-Cl by Si-F bonds at the surface. However, in our conditions, SiO_xCl_yF_z deposition is not observed on Si₃N₄ with 2 sccm of SiCl₄. Plasma exposure are systematically achieved after a chamber conditioning with a wall coating with SiO_xCl_y film. Surface analysis have been performed on Si₃N₄ after 60 s of plasma exposure without coating deposition during the conditioning sequence. No Cl2p was detected in such conditions. Consequently, we attribute Cl2p signals reported in Table 1 and Figure 4 to the wall coating etching, which probably takes place in the high plasma density region of the reactor close to the powered antenna.



Figure 4 : Quasi in-situ XPS (θ =76.25°) Cl2p and F1s atomic % measured on Si₃N₄ (green dot), Si (red square), and SiCO (orange star) in function of plasma exposure time.

At this point, highly diluted SiCl₄ (2 sccm) addition in CH₃F/O₂/He (200/200/120 sccm) plasma operated at 90 mtorr with source power of 400 W and 250 V bias voltage etches Si₃N₄ at a constant rate of 0.31 nm.s⁻¹ through a thin (< 1.6 nm) F-rich reactive layer. The same process condition leads to SiO_{1.8}F_{0.8} deposition on Si or SiCO with deposition rate in the range of 0.06 to 0.10 nm.s⁻¹. Chlorine is attributed to the chamber conditioning scheme and not to the plasma etch/deposition process itself.

B. Influence of Si₃N₄ oxidation

To figure out why in our conditions Si₃N₄ is etched while Si, SiO₂, and SiCO surfaces lead to the growth of an oxifluoride layer, the oxidation of Si₃N₄ surface by O₂ plasma and its impact on etch or deposition is explored. Figure 5 presents Si2p spectra

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evolution with take-off angle for pristine Si_3N_4 (a) and after exposure to O₂ plasma during 20 s with no bias (b) or with 100 V bias (c) applied on the substrate. On pristine Si_3N_4 , whatever the angle, contribution attributed to Si-N bonds is more intense than the one of Si-O bonds. At grazing angle, both contributions tend toward a similar intensity. These variations could be interpreted as the presence of a thin oxidized surface layer on top of pristine Si₃N₄. After O₂ plasma, Si-O contribution is more intense and Si-O to Si-N area ratio grows with the increase of the take-off angle. These tendencies are even more pronounced with 100 V bias voltage applied to the substrate. It suggests that O₂ plasma treatment induces a thicker oxidized surface layer. Its thickness increases with the use of bias voltage. We use the method first introduced by Carlson and McGuire²⁵ to estimate the over-layer oxide thickness. We exclude 69 and 76° grazing angle data and we use a constant 1.2 eV chemical shift between Si-N and Si-O²⁶ in the fitting procedure. We assume a similar inelastic mean free path of 3.5 nm for electron escaping Si₃N₄ or SiO₂ and use by simplification an intensity ratio of 1 between bulk Si₃N₄ and SiO₂. Under these assumptions, fitting the evolution of Si-O to Si-N area ratio with the reverse of the take-off angle cosine leads to oxide over-layer thickness of 0.7, 1.7, and 2.2 nm for pristine Si₃N₄, after O₂ plasma with no applied bias, and after O₂ plasma with 100 V applied bias, respectively.

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Figure 5 : Take-off angle relative to surface normal dependence of Si2p highresolution spectra measured on as deposited Si_3N_4 (a) and after O₂ plasma treatment with no bias (b) or 100 Vb (c).

The impact of these surface states on the ability to etch Si₃N₄ in highly diluted SiCl₄ is studied. As discussed in section A, without deliberate oxidation, a Si₃N₄ consumption of 18.1 nm is observed after 60 s of plasma exposure. When O₂ plasma is performed without bias, this value is reduced to 7.0 nm. Finally, on surface treated by O₂ plasma with 100 V bias, which presents a thicker oxide layer, no consumption but a 1.8 nm deposit is measured. Corresponding surface compositions are reported in Table 1. Surface compositions after SiCl₄ based plasma exposure are very similar for surfaces pre-oxidized

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by O_2 plasma with or without bias. Only a small difference is observed. Compared to pristine Si₃N₄, wafer with O₂ plasma pre-treatment presents an increase of Si-O and O-Si contributions of 0.9 and 2.4 %, respectively. It indicates a slightly more oxidized surface on wafer with O_2 pre-treatment than on pristine Si₃N₄. It could explain the drop of Si₃N₄ consumption from 18 to 7 nm by the displacement of the etch/deposition equilibrium toward more deposition promoted by a thicker surface oxide layer. This interpretation is reinforced by the surface composition of the most pre-oxidized case, which indicates the presence of SiO_{1,5}F_{0,9} on top of Si₃N₄ when SiCl₄ based plasma exposure is used on surface pre-treated by O_2 plasma with 100 V bias. These results point out the influence of the Si₃N₄ surface oxide thickness on the etch/deposition equilibrium with a switch toward etching when the initial oxide thickness is thinner than 1.7 nm. At the opposite, O₂ plasma with 100 V bias voltage forms an oxidized surface layer sufficiently thick to stop the Si_3N_4 etching and leads to oxifluoride deposition. Note that in these conditions, the deposition rate on Si_3N_4 remains 2 to 3 times lower than on Si and SiCO. Why etching occurs in highly oxidized plasma under 250 V bias voltage? How the equilibrium between reactive layer thickness, oxide formation, and etching is modified by bias voltage? Next section explores these questions of the influence of substrate bias voltage on Si₃N₄ etching.

C. Role of ion energy

Figure 6 presents the influence of substrate bias voltage on the Si₃N₄ consumption thickness or overlayer oxide deposition thickness after 60 s of plasma processing. These experiments are performed on HF last films in order to limit the impact of native oxide. Quasi *in-situ* XPS Si2p high resolution spectra acquired after plasma processing at 0 (black), 150 (red) and 250 V (green) bias voltage as well as a reference Si₃N₄ film freshly

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treated by HF (dashed black line) are shown in the Figure 6 inset. These spectra are normalized to the peak at 101.8 eV attributed to Si-N contribution, stressing the impact of the process on Si-O environment at higher binding energy. With 0 V bias applied on the substrate, Si₃N₄ is not consumed and a 3.0 nm oxide overlayer is measured. This is consistent with the Si-O to Si-N ratio larger than 1 which is observed on associated Si2p spectra. When the bias voltage increases, a transition between deposition and etching occurs. Such transition is also clearly visible on Si2p spectra where Si-O contribution strongly decreases in favor of Si-N environment. However, in comparison with HF last Si₃N₄ surface, Si₂p spectra measured after plasma processing with 250 V bias voltage still presents a shoulder on its high energy side. We attribute such shoulder to the reactive layer on top of bulk Si₃N₄. Its relative intensity decreases with the bias voltage increasing, suggesting that in the etching regime, the reactive layer thickness is thinned by the etch products removal. In pure O_2 plasma, which are not suited to etch Si₃N₄, previous results showed that the oxide thickness formed on top of Si₃N₄ increases with the rise of ion bombardment energy. In CH₃ $F/O_2/He$ (200/200/120 sccm) plasma chemistry with 2 sccm of SiCl₄, the ion energy increase leads to a rise of the etch rate and a thinning of the reactive layer thickness.

Comparable experiments are conducted on Si. Without bias voltage applied to the substrate, a 3.1 nm thick oxide overlayer is observed. This value is similar to the one measured on Si_3N_4 . By contrast, the rise of the bias voltage from 0 to 350 V conducts to an increase of the oxide overlayer from 3.1 to 4.8 nm. Measurement of the Si film thickness after oxide removal by HF wet treatment indicates that the Si consumption remains below 1.0 nm, even for the more energetic condition with 350 V bias voltage.



Figure 6 : Influence of substrate bias voltage on Si₃N₄ consumption (full dot) or oxide overlayer (open dot) thicknesses. Plasma duration is 60 s. Si₃N₄ films are deoxidized in HF 1% during 60 s prior running plasma experiments. Inset presents XPS high resolution spectra (θ =23.75°) of Si2p before (black dashed line) and after plasma processing with 0 V (black), 150 V (red), and 250 V (green) bias voltage.

D. Discussion

To sum up, relatively high bias voltage of 250 V and the use of an oxidizing chemistry with the addition of a small amount of SiCl₄ (<0.4% of the total flow) leads to the deposition of silicon oxifluoride layer on Si, SiCO, and SiO₂ materials. The deposition rate increases linearly with the rise of SiCl₄ flow from 2 to 10 sccm. On Si₃N₄, deposition is also observed when the surface presents an oxidized overlayer thicker than 2.2 nm. It turns toward etching if the oxidized layer is thinner than 1.7 nm. The etch rate decreases with the bias voltage reduction until silicon oxifluoride deposition dominates at 0 bias voltage, even on deoxidized HF last Si₃N₄ surface. These results show that a competition takes place between silicon oxifluoride deposition and Si₃N₄ etching. The equilibrium

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depends on substrate material, surface oxide layer thickness, and ions bombardment energy.

Oxychloride deposition on chamber wall have been studied in Cl_2/O_2^{22} or $HBr/Cl_2/O_2^{24}$ chemistries used in Si gate or trench patterning. These studies suggest that silicon oxychloride is deposited through oxidation of SiCl_x molecules adsorbed on chamber wall²² and that the film composition is not constant through its thickness²⁴. The deposited layer could be etched in F-rich SF₆ chemistry through film fluorination by abstraction²³ or substitution²⁴ of Cl by F atoms, leading to silicon oxyfluoride film.

What is specific to Si₃N₄ in comparison of Si, SiO₂ or SiCO to explain that etching is favored over silicon oxifluoride deposition? The larger Si-O (800 kJ.mol⁻¹) than Si-N $(437 \text{ kJ.mol}^{-1})$ bond energy²⁷ is sometimes assumed as the cause of etch selectivity^{15,21,28}. However, published data present some discrepancies with bond energy of Si-O close to the bond energy of Si-N (368 and 335 kJ.mol⁻¹, respectively²⁹). Bond energy or strength is defined as the enthalpy of dissociation of diatomic molecules in gaseous state. Its relevance to explain plasma surface interaction is questionable. In order to progress, we analyze the OES acquired during the etching of 53 nm thick Si₃N₄ layer deposited on 10 nm thick SiO₂ on Si. According to Si₃N₄ consumption kinetics presented above, 170 s are needed to remove the layer before landing on SiO₂. On Figure 7 is plotted the difference of optical spectra acquired at 180 and 140 s, i.e. after landing on SiO₂ and during Si₃N₄ etching, respectively. Such representation highlights substrate material sensitive lines, with negative (positive) intensities attributed to decreasing (increasing) lines when the transition from Si₃N₄ etching to deposition on SiO₂ takes place. Species are attributed to the wavelengths as reported by Piet et al¹⁸, the transition from Si₃N₄ etching to deposition on SiO₂ leads to the reduction of NH, N₂ and CN lines intensities while lines attributed to O and H increase.



Figure 7 : Difference of optical emission spectra acquired at 140 and 180 s during plasma processing of a 53 nm thick Si₃N₄ on 10 nm SiO₂ on Si wafer. The Si₃N₄ layer is expected to be fully etched after 170 s.

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Figure 8 : Time dependence of NH, CN, He, H (divided by 10) and O optical lines intensities (left axis) and substrate power applied to the wafer for 250 bias voltage (right axis) during plasma processing of a 53 nm thick Si₃N₄ on 10 nm SiO₂ on Si wafer.

The time evolution of these lines and the substrate power required to maintain 250 bias voltage are plotted on Figure 8. First seconds exhibit strong variations attributed to the plasma strike. Then, signals do not vary until a transition appears between 155 and 175 s. It confirms previous kinetics results expecting the end of Si₃N₄ etching at 170 s. Above all, these results give interesting insight to improve the understanding of Si₃N₄ etching mechanisms and its selectivity to SiO₂. The decrease of CN and NH lines intensities at the end of Si₃N₄ removal indicates that the formation of these species is a path to remove N from Si₃N₄. Consequently, it suggests that C and H are consumed during Si₃N₄ etching. In addition, volatile NO could be formed but no clear evidence were found in our data. The increase of H and O when silicon oxifluoride takes place on SiO₂ could be interpreted as the ending of H and O radicals consumption by reactions with N and CN to form volatiles

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NH, NO, and HCN by-products. Note that the rise of these line intensities are also partially due to a plasma density increase as indicated by the larger bias power needed to maintain 250 bias voltage. However, such increase is still present after normalization by the intensity of He line at 587.7 nm, meaning that it also denotes a specie density increase. These observations highlight the role of N by formation of CN which could react with H to form volatile HCN species^{30,31} and remove the reactive polymer surface layer. Classically, in fluorocarbon based chemistry, SiO₂ etching and its selectivity to Si invokes the role of O as a polymer inhibitor associated to CO or CO₂ formation. Here, in presence of SiCl₄, we have seen that oxidized surfaces favors the growth of an oxifluoride layer, which should take the lead over polymer layer removal. On Si₃N₄ surface, with sufficient bias power applied to the substrate, our experimental results and interpretations suggest that the Si₃N₄ etching selectivity over Si, SiO₂ or SiCO could be attributed to the formation of CN etch products which limits reactive layer thickness and allow a steady removal of etch products.

IV. CONCLUSION.

The addition of small amount of SiCl₄ to CH₃F/O₂/He plasma on Si₃N₄, c-Si, SiO₂ and SiCO etching and oxide overlayer deposition has been investigated. A strong correlation is observed between the amount of SiCl₄ added to the plasma and the oxifluoride deposition rate on all materials. If deposition occurs systematically on Si, SiO₂ and SiCO even at low SiCl₄ flow, on Si₃N₄ the amount of SiCl₄ added controls at the first order the etching (low SiCl₄ flow) or the deposition (high SiCl₄ flow) behavior. Depending if the Si₃N₄ surface is slightly or strongly oxidized, a significant etch slowdown or the deposition of a thin oxifluoride is observed, respectively. It strongly indicates that

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oxidation state is a major Si₃N₄ etch inhibitor using CH₃F/O₂/He/SiCl₄ chemistry and it is consistent with the deposition behavior previously observed on other silicon-based oxidized materials, such as SiO₂ and SiCO. In addition, oxifluoride deposition occurs on Si₃N₄ at no or low bias voltage applied to the substrate. Such deposition quickly decreases when bias voltage increases until Si₃N₄ is etched for high bias voltage (150 V or more), indicating that there is an ion energy threshold switching the equilibrium from deposition to etch. On the other side, oxifluoride deposition on c-Si increases with bias voltage suggesting that, beyond oxidation state and ion energy threshold, Si₃N₄ has some particularity compared to the other materials studied leading to a specific behavior when exposed to CH₃F/O₂/He/SiCl₄. The use of OES during Si₃N₄ etching highlights that C and H from plasma gas feed are mainly consumed through NH, CN and HCN by-products formation. We believe that the volatile by-products formation on Si₃N₄ under the right process condition (ie, sufficient bias voltage) is the key differentiator to limit reactive layer thickness and to allow a steady etch while deposition occurs on the other materials.

AUTHOR DECLARATIONS

Conflict of interest

The authors have no conflicts to disclose.

Authors Contribution

François Boulard : Writing – original draft (equal), Writing – review & editing (lead), Formal analysis (supporting), Validation (equal), Supervision (supporting)

Valentin Bacquié : Writing – original draft (equal), Writing – review & editing (supporting), Investigation, Conceptualization (equal), Formal analysis (lead), Resources,

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Aurélien Tavernier : Writing-original draft (equal), Writing – review & editing (supporting), Conceptualization (equal), Validation (equal), Supervision (supporting)

Nicolas Posseme : Funding acquisition, Conceptualization (equal), Writing – review & editing (supporting), Project administration, Supervision (lead)

DATA AVAILABILITY

The data that support the findings of this study are available upon reasonable request.

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TABLES

Element (%)		Pristine films			After 30 s plasma exposure			After 70 s plasma exposure with O ₂ plasma pre-treatment	
		Si ₃ N ₄	Si	SiCO	Si ₃ N ₄ Si SiCO			With no bias Si ₃ N ₄	With 100Vb Si ₃ N ₄
	C-C	5.4	10.4	6.4	1.5	1.5	2.5	-	-
C1s	C-O	1.4	2.0	-	-	-	-	-	-
	C=O	-	1.2	-	-	-	-	-	-
	C-Si	-	-	12.3	-	-	1.6	-	-
Cl2p	Cl-Cl	-	-	-	1.3	0.6	-	-	-
F1s	F-Si	0.6	-	-	11.7	21.5	20.6	11.7	24.5
O1s	O-Si	17.5	32.0	47.0	11.8	42.7	45.8	14.2	43.8
N1s	N-Si	38.2	-	-	38.0	-	-	38.0	2.2
	Si-O	7.6	9.0	20.9	6.2	24.1	17.4	7.1	26.6
Si2p	Si-N	29.3	-	-	29.6	-	-	29.0	2.8
	Si-C	-	-	13.4	-	-	12.1		
	Si-Si	-	45.5	-	-	10.6	-		

Table 1 : Surface composition measured by quasi in-situ XPS ($\theta = 23.75^{\circ}$) on Si, SiCO, and Si₃N₄ before and after plasma exposure. Pristine films are measured ex-situ. Measurements with additional O₂ plasma pre-treatment are also reported.

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FIGURE CAPTIONS

Figure 1 : Dependence on SiCl4 flow rate of overlayer deposition rate on Si $_3N_4$, Si, SiO $_2$ and SiCO.

Figure 2 : Si₃N₄ (left axis), Si, and SiCO (right axis) consumption versus plasma exposure time for 2 sccm SiCl₄ flow rate. Measurements after HF 1% 30s treatment are presented by open symbols.

Figure 3 : Si2p high resolution spectra at 23.75° relative to surface normal takeoff angle on Si₃N₄ (a), Si (b), and SiCO (c) after 30 and 60 s of plasma exposure with 2 sccm SiCl₂ flow rate and vacuum transfer to XPS analysis chamber. Pristine films are measured ex-situ.

Figure 4 : Quasi in-situ XPS (θ =76.25°) Cl2p and F1s atomic % measured on Si₃N₄ (green dot), Si (red square), and SiCO (orange star) in function of plasma exposure time.

Figure 5 : Take-off angle relative to surface normal dependence of Si2p highresolution spectra measured on as deposited Si_3N_4 (a) and after O₂ plasma treatment with no bias (b) or 100 Vb (c).

Figure 6 : Influence of substrate bias voltage on Si₃N₄ consumption (full dot) or oxide overlayer (open dot) thicknesses. Plasma duration is 60 s. Si₃N₄ films are deoxidized in HF 1% during 60 s prior running plasma experiments. Inset presents XPS high resolution spectra (θ =23.75°) of Si2p before (black dashed line) and after plasma processing with 0 V (black), 150 V (red), and 250 V (green) bias voltage.

Figure 7 : Difference of optical emission spectra acquired at 140 and 180 s during plasma processing of a 53 nm thick Si₃N₄ on 10 nm SiO₂ on Si wafer. The Si₃N₄ layer is expected to be fully etched after 170 s.

Figure 8 : Time dependence of NH, CN, He, H (divided by 10) and O optical lines intensities (left axis) and substrate power applied to the wafer for 250 bias voltage (right axis) during plasma processing of a 53 nm thick Si₃N₄ on 10 nm SiO₂ on Si wafer.



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