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M. Lisowska, J. Bortfeldt, F. Brunbauer, G. Fanourakis, K.J. Floethner, et al.. Sub-25 ps timing measurements with 10 x 10 cm 2 PICOSEC Micromegas detectors. Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2022, pp.167687. 10.1016/j.nima.2022.167687 . cea-03844611

HAL Id: cea-03844611 https://cea.hal.science/cea-03844611

Submitted on 8 Nov 2022

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PII:	S0168-9002(22)00979-2
DOI:	https://doi.org/10.1016/j.nima.2022.167687
Reference:	NIMA 167687

To appear in: Nuclear Inst. and Methods in Physics Research, A

Received date : 31 August 2022 Revised date : 17 October 2022 Accepted date : 19 October 2022

Please cite this article as: M. Lisowska, J. Bortfeldt, F. Brunbauer et al., Sub-25 ps timing measurements with 10×10 cm² PICOSEC Micromegas detectors, *Nuclear Inst. and Methods in Physics Research, A* (2022), doi: https://doi.org/10.1016/j.nima.2022.167687.

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Sub-25 ps timing measurements with $10 \times 10 \text{ cm}^2$ PICOSEC Micromegas detectors

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Preprint submitted to NIM A

October 17, 2022

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Abstract

The PICOSEC Micromegas detector is a precise timing gaseous detector based on a Cherenkov radiator coupled to a semi-transparent photocathode and a Micromegas amplifying structure. First single-pad prototypes demonstrated a time resolution below $\sigma = 25$ ps, however, to make the concept appropriate to physics applications, several developments are required. The objective of this work was to achieve an equivalent time resolution for a 10×10 cm² area PICOSEC Micromegas detector. The prototype was designed, produced and tested in the laboratory and successfully operated with a 80 GeV/c muon beam. Preliminary results for this device equipped with a CsI photocathode demonstrated a time resolution below $\sigma = 25$ ps for all measured pads. The time resolution was reduced to be below $\sigma = 18$ ps by decreasing the drift gap to 180 µm and using dedicated RF amplifier cards as new electronics. The excellent timing performance of the single-channel proof of concept was not only transferred to the 100-channel prototype, but even improved, making the PICOSEC Micromegas detector more suitable for large-area experiments in need of detectors with high time resolutions. Keywords: Gaseous detectors, Micromegas, Multipad, Timing resolution

1 1. Introduction

The challenges of future High Energy Physics experiments, including 2 high-pile-up environments expected in the High-Luminosity Large Hadron 3 Collider (HL-LHC), have aroused intense interest in the development of 4 technologies for detectors with high time resolution. A time resolution of 5 tens of picoseconds, stable long-term operation and a large area coverage 6 are key requirements that these devices must fulfil to be applicable in de-7 manding environments such as the HL-LHC. The first PICOSEC Micromegas 8 (MM) prototype indicated how precise time resolution can be achieved with 9 a single-channel gaseous detector with an active area of 1 cm in diameter 10 [1]. Although the detector's proof of concept demonstrated a $\sigma = 25$ ps time 11 resolution, this excellent timing performance has never been transferred to a 12 100-channel device. The objective of this work was to achieve an equivalent 13 time resolution for the 100-channel PICOSEC MM detector with an active 14 area of 10×10 cm². 15

In Section 2, the PICOSEC MM detection concept is outlined. Section 3 16 is a description of the 10×10 cm² area PICOSEC MM detector design and 17 production. In Section 4, the electronics dedicated to the 100 channels detec-18 tor, including custom-made amplifiers and SAMPIC digitizer, are explained. 19 The results from timing measurements obtained for the 10×10 cm² multi-20 pad detectors are presented and discussed in Section 5. Section 6 gives an 21 outlook of the PICOSEC MM detector robustness aspects that need to be 22 implemented, including resistive MM and robust photocathodes. Section 7 23 is a summary of the conclusions drawn from this paper. 24

25 2. PICOSEC Micromegas detection concept

Within the PICOSEC MM collaboration, a gaseous detector that aims at 26 reaching a time resolution of tens of picoseconds is being developed [1]. The 27 PICOSEC MM detection concept is presented in Fig. 1. A charged particle 28 passing through a Cherenkov radiator (3 mm thick MgF_2 crystal) creates a 29 cone of UV photons that are converted into photoelectrons on the photo-30 cathode. The baseline converter of the UV photons into the photoelectrons 31 for the PICOSEC MM detector is a 18 nm thick Cesium Iodide (CsI) semi-32 transparent photocathode. Following the extraction of the primary electrons 33 from the photocathode, a high electric field in the drift gap leads to succes-34 sive ionisations by the primary electrons and thus a pre-amplification. After 35 passing through the MM mesh, the electrons are additionally multiplied in 36 the amplification gap. Both regions are filled with a flammable gas mixture 37 of 80% neon, 10% CF₄ and 10% ethane. The amplified electrons induce a 38 signal on the anode which is passed through an amplifier and read-out by a 39 digitizer. A typical PICOSEC MM waveform displaying a fast electron peak 40 and a slow ion tail is presented in Fig. 2. The rising edge of the electron 41 peak is used to calculate the time of arrival. 42

The first single-pad prototypes demonstrated time resolution below $\sigma =$ 25 ps [1]. To make the concept appropriate to physics applications, several developments are required. Since the objective is to build a tileable multichannel detector modules for large area coverage, the project progressed from a single-pad prototype [1] to a 19-channels device [2] before the current 100channel module [3] which is the main subject of this study. The active area of the detector changed from 1 cm to 3.6 cm diameter circle between single-



Figure 1: Schematic representation of the PICOSEC detection concept. A charged particle passing through a Cherenkov radiator creates UV photons that are converted into primary electrons on a photocathode, pre-amplified in the drift gap and multiplied in the amplification gap [3].

and multi-pad prototypes and it was scaled up to 10×10 cm² for the 100channel module, increasing the surface almost 10 times in comparison to the 19-channels device. The evolution of the PICOSEC MM detectors is presented in Fig. 3. The detector's mechanics, electronics and robustness are some of the developments towards applicable device that are presented in this work.

⁵⁶ 3. Design and production of the 10×10 cm² area PICOSEC Mi ⁵⁷ cromegas detector

The PICOSEC MM detectors consist of a printed circuit board (PCB) on top of which a woven mesh of 18 µm thick steel wires is stretched over pillars and a coverlay (Pyralux PC1025) frame. The thickness of the amplification gap is defined by the height of the bottom coverlay and the pillars and chosen to be 128 µm. The thickness of the drift gap is defined by the spacer placed on top of the mesh stretching frame which in the standard configuration is



Figure 2: A typical PICOSEC MM waveform displaying a fast electron peak and a slow ion tail [3].

64 200 μm.

The first PICOSEC MM multipad prototype was a 19-channels detector 65 with a circular-shaped active area of 3.6 cm in diameter and a PCB made 66 of a 3.2 mm thick FR4 plate 2. Preliminary measurements of the detector 67 flatness showed deformations in the active area in the range of $30 \ \mu m$ that 68 caused a non-uniform drift gap and resulted in a non-uniform response of 69 the device. The deformations were introduced by mechanical stress on the 70 PCB due to fixing it to the detector flange as well as non-flatness of the PCB 71 because of the forces originating from stretching the mesh on top of it. For 72 scaling-up this prototype, the problem of the deformations was anticipated 73 to be more pronounced for the larger active areas. The requirement for 74 the 10×10 cm² module was to obtain precise mechanical parts to preserve 75 uniform thickness of the drift gap. 76

In order to improve the flatness of the active area, the necessary steps were to decouple the PCB from the flange by using a second board called



Figure 3: The evolution of thw PICOSEC MM detectors. From left to right: a single-pad prototype [1], a 19-channels device [2], the 100-channel module [3]. Note that the figures are not in scale.

outer PCB and design a more rigid MM PCB [3, 4], as can be seen in Fig. 79 4. To avoid the mechanical connection of the MM PCB to the housing, a 80 new design was made which includes a 6 mm thick outer PCB screwed to the 81 aluminum housing. The outer PCB was connected with the MM PCB with 82 spring-loaded pins. To choose more rigid MM PCB material and its proper 83 thickness, structural simulations of the board deformations under mesh ten-84 sion were performed using open-source tools: FreeCAD, Gmsh and CalculiX 85 [5]. Based on the experience from the first 19-channels prototype, the main 86 objective for the 10×10 cm² module was to minimise the deformations below 87 10 µm throughout the whole active area. The calculations included analysis 88 of how the mesh tension and contact pressure influence the planarity. Sim-89 ulations suggested that a big gain in planarity can be achieved by a change 90 of the material and only a small increase in the ceramics thickness, as can 91 be seen in Table A.1. The influence of the mesh tension on the planarity of 92 a 4 mm thick ceramic board resulted in maximum calculated displacement 93

⁹⁴ in the active area of around 4 µm, which is presented in Fig. 5. The final
⁹⁵ design of the MM PCB, shown in Fig. 6, consists of a 4.85 mm thick hybrid
⁹⁶ ceramic-FR4 board instead of a 3 mm thick pure FR4 one.



Figure 4: A new PICOSEC MM detector design consisting of 2 separate PCBs: an outer board screwed to the aluminum housing and a more rigid hybrid ceramic-FR4 MM board. The outer PCB was connected with the MM PCB with spring-loaded pins [3].

The production of the hybrid ceramic-FR4 board was monitored with planarity measurements using a Keyence 3D microscope from the beginning of the process [6]. The analysis of the board before the bulking showed that its planarity was within the specifications, as can be seen in Fig. 7. The 100 channels PICOSEC MM detector with flatness deformations below 10 µm was successfully produced, preserving uniform thickness of the drift gap.

¹⁰³ 4. Electronics dedicated to the 100 channels detector

104 4.1. Custom-made amplifiers

The primary used electronics for amplifying signals induced on the anode was the CIVIDEC C2 [7], a low-noise amplifier with an analog bandwidth of 2 GHz, a gain of 40 dB, and an oscilloscope for digitising waveforms. However, the use of 100 CIVIDECs is highly impractical when it comes to scaling to



Figure 5: A simulation of the mesh tension influence on the planarity. For a 4 mm thick ceramic PCB, a maximum displacement in the active area of around 4 µm was calculated [4].

multiple channels detectors. Therefore, several different types of custom-109 made amplifiers are being developed. A promising solution are RF pulse 110 amplifier cards optimised for the PICOSEC. The design was based on the 111 RF pulse amplifier for CVD diamond particle detectors [8] and modified with 112 a spark protection up to 350 V, a bandwidth of 650 MHz, a gain of 38 dB and 113 a power consumption of 75 mW per channel [9]. The results obtained for the 114 measurements performed with these amplifiers are presented and discussed 115 in Subsection 5.4. 116

117 4.2. SAMPIC digitizer

After the amplification, the signal needs to be read-out. The baseline digitizer for the PICOSEC MM detector is an oscilloscope with 10 GS/s sampling frequency. Again, the use of 100 oscilloscope channels is highly impractical when it comes to scaling to multiple channels detectors. There-



Figure 6: The final design of the MM PCB consisting of a 4,85 mm thick hybrid ceramic-FR4 board. The anode read-out pads are connected to the back side pads through vias that are plated and filled with conductive epoxy [3].

fore, the SAMPIC waveform TDC digitizer [10] is currently being tested. 122 During the last beam campaigns, a 64 channel SAMPIC was tested, while a 123 128 channel device is being implemented for reading out the PICOSEC MM 124 modules. While the maximum available sampling frequency of currently used 125 SAMPIC modules is limited to 8.5 GS/s, this appears to be sufficient to pre-126 serve the timing precision of the PICOSEC MM detectors. The principle 127 of the complete readout chain was proven for the first time by a successful 128 readout of 50 multipad PICOSEC channels. 129

$_{130}$ 5. Timing measurements of the 10×10 cm² area multipad PICOSEC

131 5.1. Experimental setup

The 100 channels PICOSEC MM detectors were tested in the laboratory and successfully operated during test beam campaigns. The main purpose of the campaigns was to measure timing resolution of the prototypes assembled in different configurations. The measurements were performed at the CERN SPS H4 beam line with a 80 GeV/c muon beam. The main part of the



Figure 7: Planarity measurements of the top half of the PICOSEC Micromegas detector showing flatness deformations below 10 µm [6].

test setup consisted of a beam telescope with triggering, timing and tracking capabilities. An example of a telescope configuration is presented in Fig. 8. The precise tracking of the particles was done using triple-GEMs with a spatial resolution below 80 µm. Multiple micro-channel plate photomultiplier tubes (MCP-PMTs) were used as timing reference and data acquisition (DAQ) trigger. The telescope was built in such a way that it could perform tests of several devices at the same time.



Figure 8: An example of a telescope configuration consisting of GEM detectors for precise tracking (in blue), MCP-PMTs for timing reference and DAQ (in green) and devices under test (in red). DAQ could also be done by using a coincidence of four scintillators (in yellow).

144 5.2. Time resolution analysis procedure

To properly quantify the timing precision of the PICOSEC detectors, 145 a reference device with better timing precision was required. In the work 146 presented here, an MCP-PMT (R3809U-50 Hamamatsu) with a uniform re-147 sponse in the area of 11 mm in diameter was used as a timing reference of 148 the PICOSEC prototype. The devices were spatially aligned with respect to 149 each other, so when muons passed through them, signals on both detectors 150 were induced. In the analysis, a sigmoid functions were fitted to the lead-151 ing edges of the electron peaks giving the positions of the signals in time 152 at the 20% Constant Fraction (CF) [3]. The signal arrival time (SAT) was 153 defined as a difference between PICOSEC detector and reference device tim-154 ing marks. The time resolution of the detector was calculated as a standard 155 deviation of the SAT distribution. The contribution of the MCP-PMT was 156 not subtracted from the time resolution of the PICOSEC detector for all the 157 measurements presented in this work. 158

5.3. Time response of the multipad PICOSEC in the standard configuration 159 First test beam measurements of the multipad PICOSEC were performed 160 using the "standard" configuration of the prototype: non-resistive detector 161 with 220 μ m drift gap, 3 mm MgF₂ radiator with CsI photocathode and 162 the CIVIDEC amplifier. The aim of the experiment was to obtain timing 163 resolution when signals were fully contained within the center of individual 164 pads within 5×5 mm² area. Preliminary results show uniform time resolution 165 of $\sigma = 25$ ps or below for all measured pads [3], confirming that the excellent 166 timing performance can be transferred from a single-pad prototype to a 100-167 channel module. An example of a SAT distribution of one channel with time 168

resolution, obtained by using the standard deviation, is shown in Fig. 9 andthe results from all tested pads are summarised in Table A.2.



Figure 9: SAT distribution of one channel of the multipad PICOSEC while performing test beam measurements in the "standard" configuration of the detector. The voltage on the cathode was 500 V and on the anode 275 V. The histogram consist of the data after implementing a geometrical cut which was a 4 mm diameter circle made in the center of the pad. Time resolution of the measured channel was $\sigma = 25.0$ ps.

When a particle hits the detector in between the pads or in the corner of 171 the active area and the induced signal is not fully contained inside a single 172 channel, the timing capabilities of that channel are reduced [2]. Therefore, 173 sharing of the signal between 4 neighbouring pads was studied in order to test 174 the possibility of timing reconstruction. For this test the same "standard" 175 configuration of the multipad PICOSEC was used. The measurement was 176 performed using the MCP-PMT centered in the cross between 4 channels 177 of the detector and the signals from both devices were sufficiently collected 178 during one run. The experiment confirmed that timing information can be 179

reconstructed when the signal is shared between pads. The obtained time resolution was $\sigma = 30.0$ ps [11, 12].

5.4. Time response of the multipad PICOSEC with the custom-made ampli fier cards and the reduced drift gap

First test beam measurements with custom-made RF pulse amplifiers 184 optimised for the PICOSEC MM detectors were done as a next step towards 185 the improvement of the time response of the device. Except for the new 186 amplifiers, the detector was in the "standard" configuration. Results from 187 the signals fully contained within the center of individual pads within 5×5 188 mm^2 area confirmed the improvement of the time resolution. This may be 189 attributed to the lower bandwidth, which suppress the noise components, 190 and better impedance matching of the new amplifiers to the detector in 191 comparison to the CIVIDEC electronics. The time resolution obtained for 192 these measurements was below $\sigma = 22$ ps for all tested pads [9]. An example 193 of a SAT distribution of one channel is shown in Fig. 10 and results from all 194 measured pads are summarised in Table A.3. 195

A final adjustment towards improving the time resolution of the PI-196 COSEC MM detector was the reduction of the drift gap. The time resolution 197 depends on the SAT which can be reduced by employing a higher drift field 198 [13]. The higher drift field can be achieved by applying higher voltages on 199 the electrodes, however, when too much charge is produced, the operation 200 of the detector becomes unstable. An alternative solution is reducing the 201 thickness of the drift gap. The thinner the gap, the shorter the distance 202 between successive ionisations. For a detector with fixed operating voltages 203 and a decreased drift gap, the electric field is higher, thereby improving the 204



Figure 10: SAT distribution of one channel of the multipad PICOSEC while performing test beam measurements with the dedicated RF pulse amplifier cards. The voltage on the cathode was 485 V and on the anode 275 V. A geometrical cut of 4 mm diameter circle was made in the center of the pad. Time resolution of $\sigma = 20.2$ ps was measured.

time resolution. At the same time, the distance of the pre-amplification is 205 shorter and the gain is smaller, ensuring a stable operation of the device at 206 a high electric field. Fig. 11 presents a schematic design of the multipad 207 PICOSEC with the drift gap reduced to 180 µm. The measurements of this 208 prototype equipped with a CsI photocathode and the dedicated RF pulse 209 amplifier cards showed a time resolution below $\sigma = 18$ ps for all measured 210 pads [9]. An example of SAT distribution of one channel with time resolution 211 of $\sigma = 16.7$ ps is presented in Fig. 12. 212



Figure 11: The PICOSEC MM detector with the drift gap reduced form 220 µm to 180 µm in order to improve time resolution [9].

213 6. Robustness of the PICOSEC Micromegas detector

214 6.1. Resistive Micromegas

In order to build an applicable device that can be operated in high-rate 215 environments, the PICOSEC MM detector needs to be made more robust. 216 The "standard" configuration of the PICOSEC prototype uses a non-resistive 217 MM detector. Studies on resistive MM are being performed to limit the de-218 structive effect of discharges and achieve stable operation in intense pion 219 beams with a resistive anode. First tests with small area (1 cm in diameter) 220 resistive MM with anode surface resistivitites of 292 k Ω/\Box and CsI photo-221 cathode showed a time resolution of $\sigma = 24.1$ ps. Measurements with small 222 area resistive MM of 82 M Ω / \Box and Diamond Like Carbon (DLC) photocath-223 ode presented time resolution below $\sigma = 40$ ps, driven by the lower number 224 of photoelectrons produced in the photocathode material. A multipad PI-225 COSEC with 10×10 cm² resistive MM of 20 M Ω/\Box is being prepared to be 226 measured in the next test beam campaign. 227

228 6.2. Robust photocathodes

The first single-pad prototype used a CsI photocathode as a converter of UV photons from the Cherenkov cone into photoelectrons. CsI is charac-



Figure 12: SAT distribution of one channel of the multipad PICOSEC while performing test beam measurements with the drift gap reduced form 220 µm to 180 µm. A geometrical cut of 4 mm diameter circle was made in the center of the pad. The voltage on the cathode was 460 V and on the anode 275 V. Time resolution of $\sigma = 16.7$ ps was measured.

terised by its high quantum efficiency (around 10 photoelectrons produced 231 per minimum ionizing particle with 3 mm thick MgF_2 radiator [1]) in com-232 parison to other materials studied up to now. However, it can be easily 233 damaged by ion back flow, sparks, discharges and it is sensitive to humidity. 234 Therefore, there is a need to search for alternative photocathode materials 235 that would be more robust. The most promising candidates are DLC, Boron 236 Carbide (B_4C) or nanodiamonds. First measurements with the multipad 237 PICOSEC with a DLC photocathode, a 220 µm drift gap and CIVIDEC 238 amplifier, showed time resolution of $\sigma = 45$ ps for all measured pads [14]. 239 Additionally, comparative measurements with different thicknesses of B_4C 240 photocathodes in small single-channel prototypes are ongoing. 241

242 **7.** Conclusions

Preliminary results for the 10×10 cm² area PICOSEC detector demon-243 strated a time resolution below $\sigma = 25$ ps per pad, showing that the excel-244 lent timing performance of the PICOSEC detector's single-channel proof of 245 concept can be transferred to the 100-channel prototype. The best time res-246 olution, below $\sigma = 18$ ps, was obtained for the detector with the 180 µm drift 247 gap, a CsI photocathode and the dedicated RF pulse amplifier cards. The 248 results made the concept more suitable for large-area experiments in need of 249 detectors with high time resolution. Developments towards applicable detec-250 tors are ongoing. They include the improvement of stability of the detector 251 by using the multipad PICOSEC with a resistive MM and robustness studies 252 of alternative photocathode materials, e.g. DLC and B_4C . As a next step, 253 a complete readout of a 100 channel prototype with the dedicated RF pulse 254 amplifier cards and SAMPIC waveform TDC digitiser is planned. Addition-255 ally, sealed detectors, which are clean, hermetically closed devices with high 256 gas quality, are being developed. Scaling the PICOSEC MM detector to 257 larger area by tiling 10×10 cm² modules or the development of 20×20 cm² 258 prototype is considered to be a next step towards applicable devices. 259

260 Acknowledgements

We acknowledge the financial support of the EP R&D, CERN Strategic Programme on Technologies for Future Experiments; the RD51 collaboration, in the framework of RD51 common projects; the Cross-Disciplinary Program on Instrumentation and Detection of CEA, the French Alternative Energies and Atomic Energy Commission; the PHENIICS Doctoral School Program

of Université Paris-Saclay; the Fundamental Research Funds for the Central
Universities of China; the Program of National Natural Science Foundation
of China (grant number 11935014); the COFUND-FP-CERN-2014 program
(grant number 665779); the Fundação para a Ciência e a Tecnologia (FCT),
Portugal (grants IF/00410/2012 and CERN/FIS-PAR/0006/2017); the Enhanced Eurotalents program (PCOFUND-GA-2013-600382); the US CMS
program under DOE contract No. DE-AC02-07CH11359.

273 Appendix A.

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Table A.1: A comparison of the maximum estimated displacement in the active area for PCBs made of different materials and thicknesses given by simulations [4].

PCB	Thicknes	s Displace	nent
mate-	[mm]	[µm]	
rial			
FR4	3	91	
67	6	20	
	9	10	
Hybrid	2	17	
ceramic-	3	8	
FR4			
7	4	4	

Pad number	Time	
	resolution	
	[ps]	
03	24.6	
06	24.1	
12	25.0	
13	23.9	
15	22.1	
16	22.9	
17	24.7	
18	23.0	
20	23.0	
26	23.9	
36	23.9	
41	24.0	

 Table A.2: Time resolution of all measured pads for the multipad PICOSEC in the

 "standard" configuration [3].

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Pad number	Time	
	resolution	
	[ps]	
12	21.1	
13	19.2	
15	20.2	
18	19.5	
22	18.9	
25	20.6	
26	20.8	
31	21.1	
36	21.0	
40	20.4	

Table A.3: Time resolution of all measured pads for the multipad PICOSEC with the custom-made amplifier cards [9].

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