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Enhancing the Spatial Resolution for Wire Fault Detection Systems using Multi-Carrier Signals

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Abstract—Recently, the world is witnessing a vast daily development in technology accompanied with more and more complex electronic systems. They are hosting cumulated lengths of electrical cables that are subject one day or another to the occurrence of wiring faults. Accordingly, wire diagnosis became essentially important for ensuring safety, security, integrity and optimal performance. On the other hand, the emergence of sensor networks and connected objects have created the need for embedded and non invasive fault diagnosis solutions. Notably, multi-carrier reflectometry methods have shown promising and efficient results in locating upcoming defects on wires in an online manner. Nevertheless, their precision stays within the physical limits of their components, especially the sampling frequency of their analog/digital transitions. In this paper, we will propose several approaches combining multi-carrier reflectometry with phase analysis techniques to overcome this limit. Accordingly, an improvement in the precision of the fault localization and a high spatial resolution is obtained. Based on an FPGA implementation, our novel methods and the resulting systems have proven a five-times better accuracy than state-of-the-art methods on the same platform.

Keywords—*Reflectometry, phase analysis, Fourier transform, multi-carrier, defect diagnosis, high precision sensing.*

I. INTRODUCTION

Since the invention of the first known typical electrical cable in 1892, the world is living a paradigm shift of hydraulic, pneumatic, mechanical, control and auxiliary systems to electrical systems. In fact, this was accompanied with a huge demand for electrical components along with a massive reckoning on electrical cables. For instance, they can be found in transportation systems, communication networks, industrial machinery, power plants, infrastructures, in addition to most of today's human facilities and utilities.

In practice, cables are always used in distribution networks where a set of cables, usually of the same type, interconnect according to well defined topology standards (bus, star, mesh, ring, tree, etc.). On the other hand, one day or another, any complex system embracing cumulated lengths of wired

connections is highly likely exposed to internal and external compelling conditions. These include factors not limited to heating, humidity, pressure, vibration, etc., thus leading to the appearance of anomalies often referred to as faults.

Wiring faults may be localized on a point or short section of a line or distributed on a longer length. For instance, they may lead to a total interruption of transmission as in the case of hard faults (open or short circuits) that can also electrically sever a line into two separate portions. Meanwhile, soft faults are characterized by slightly modifying the cable properties, without causing a significant impact on the whole system operation (insulation damage, frays, cracks, etc.).

Throughout the last few decades, strenuous efforts have been invested in research and industry to study and develop techniques capable of detecting the presence, location and characteristics of wiring faults. As a matter of fact, a great number of embedded systems dedicated to safety and comfort communicate with increasingly important data rates in order to fulfill severe real-time constraints. These constraints imply to have at disposal a trustworthy physical support to guarantee both quality of service and reliability. Accordingly, monitoring failures and troubleshooting cables have become an important issue in order to ensure safe operation, high performance and cost effectiveness. Incipient or soft faults have been extensively handled in current literature where recent studies have focused on the application of time reversal (TR) to the problem of soft fault location and showed high accuracy and robustness against fault impedances [1]–[6]. Impedance spectroscopy [7], inverse scattering [8] and methods based on the estimation of the per unit length resistance profile $R(z)$ of a cable under test (CUT) [9] have also proven proficiency in this mission.

While several electric and nonelectric wire diagnosis methods have been proposed, reflectometry-based methods are still the cutting edge of most embedded and distributed wire diagnosis systems [10]–[13]. Basically relying on the analysis of the electrical waves reflected on impedance mismatches (IMis) along a network under test (NUT), Time-Domain Reflectometry (TDR) injects a test signal $x(t)$ on the CUT, analyzes the behavior of the IMis's reflected echo $y(t)$ and detects the potential reflections through a correlation $\Gamma_{xy}(t)$ given by

$$\Gamma_{xy}(t) = \int_{-\infty}^{\infty} y(\tau + t) \cdot x^*(\tau) d\tau \quad (1)$$

with τ denoting the lag.

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Despite the criticality of soft fault detection due to their: 1) dispersive nature accompanied with phase pattern change of the testing signal, 2) weak reflected echoes that can be easily masked by minor vibrations in a NUT or the presence of noise, etc. [11], [14], [15], studies conducted on accident investigations of fires in facilities, airplane crashes, system breakdowns, etc. have shown a strong correlation with hard faults [16]. Although, hard faults have been the center pivot of research in the early stages of reflectometry methods, major concerns related to the accuracy and resolution of location, complexity of testing setups in addition to the application in online operating systems have been and are still forming a debatable controversial issue. More importantly is the problem of intermittent hard faults that are not permanent faults and occur in very short durations. Particularly, an arc fault creates a high power discharge of electricity between two or more conductors; this can be considered as a short circuit during a very short instant. Such sporadic faults may have duration less than 1 ms. Thus, they are not always guaranteed to be caught during a standard reflectometry measurement after which the wiring system would behave normally as if nothing happened. However, the intermittent transients are the incipient events of a precursor of permanent faults to come.

Reviewing subsequent and more recent literature [11], [17], standard TDR methods are based on the idea of testing the NUT by means of a specifically designed signal (pulsed, harmonic, or an ad hoc signal, etc.) applied to an input port coupled to the NUT. As any method based on time-domain analysis, TDR relies on the availability of potentially large bandwidths, in order to create the conditions for spatial resolution. The spatial resolution Δ_R enabled by a test signal is directly proportional to its spatial support or that of its autocorrelation function, given by

$$\Delta_R = k \frac{v}{BW} \quad (2)$$

where v is the speed of propagation of electrical signals along the cables of the NUT and BW is the bandwidth of the test signals; k is a constant of the order of unity, dependent on the shape of the test signals. Limitations are thus introduced by the ability of cables in an NUT to support such bandwidths, as in the case of low-frequency networks, such as power grids. Besides, this shall also entail complex testing setups, poor performance in terms of energy efficiency, and consequently preventing this method from being suitable for embedded systems and distributed sensors networks.

It is important to note that, live testing is of practical importance, since it does not require turning off all the electronic equipment that normally use the NUT as a power or communication infrastructure; also known as embedded testing, this approach works in the background of the normal operation of the NUT. But online network testing without impacting the functionality of a system necessitates specific testing signals that do not interfere with the useful signals operating in the network. Eventually, Spread Spectrum TDR (SSTDR) [10], Multi Carrier TDR (MCTDR) [18], [19], Orthogonal Multi-tone TDR (OMTDR) [20], [21], Chaos TDR (CTDR) [22], [23] and modulated variants of the OMTDR

[24], [25] provided promising and robust results in live cable-diagnosis. Particularly, they have allowed a complete control of the spectrum of the injected signal which shall not interfere with the system under test (SUT).

In the literature of OMTDR, the Orthogonal Frequency Division Multiplexing (OFDM) concept is used [20]. Basically, it divides the channel bandwidth into many sub-carriers independently modulated, hence achieving high data rate transmission, interference robustness against native signals on live cables, bandwidth efficiency and complete spectrum control. On the other hand, enhancing the spatial resolution and fault location accuracy relies on methods that periodically inject the testing signal into the CUT [26], [27]. Such approaches would require real-time sampling thus imposing the use of high-speed analogue-to-digital converters (ADC), which can be both expensive and power consuming. To bypass this problem, stroboscopic sampling can increase the performance of the detector by emulating a faking oversampling which periodically time-shifts the sampling phase [28]. Nevertheless, they are still costly, very slow, and totally jitter intolerant. Regrettably, their long acquisition times make them also unsuitable and inefficient for intermittent fault detection.

To cope with the aforementioned limitations, we propose in this paper several approaches based on the fusion of the OFDM multi-carrier signals used in the OMTDR with low-cost efficient stroboscopic sampling techniques and frequency domain analysis methods. The result would be the production of low complexity system architectures capable of performing relatively fast acquisitions while resulting in a high spatial resolution and precision of the fault's locations.

The paper is organized as follows. Section II describes the basic aspects of the OMTDR method during which a brief overview of the system's hardware implementation is reviewed. In this section, we show that improving the fault location accuracy is directly related to increasing the sampling frequency of the analog-to-digital and digital-to-analog converters (ADC and DAC). Section III shows that developing faster ADCs and DACs is confronted by several obstacles namely the system's complexity and cost. Accordingly, three different approaches are presented to provide an efficient, low cost and less complex solutions. The first is demonstrated by an existing stroboscopic sampling technique based on the incremental phase-offset shifting method presented in section III-A. Later on, section III-B displays a novel technique relying on the usage of a constant frequency offset. The third method is a novel approach based on the phase analysis of the multi-carrier reflected signals and is presented in section III-C. A feasibility proof is accomplished in section IV where simulation and experimental results using an FPGA platform and external analog converters are established to validate the potential reliability and efficient implementation of the proposed techniques. Section IV-C presents a comparison between the three presented methods and that of the standard OMTDR method (SOMTDR) in terms of the fault location error and acquisition duration. Finally, section V concludes this paper by addressing the final remarks on the performance of the proposed methods and draws some perspectives for future work.

II. PRINCIPLES OF THE STANDARD OMTDR SYSTEM

A simplified model of an OMTDR system is presented in Fig. 1. The sub-carriers of the reflectometry signal $X(f)$ are computed by the OFDM generator block, which performs an N -PSK digital modulation and applies a Hermitian symmetry. Each sub-carrier is defined as:

$$|X(f_n)| = 1 \quad \forall f_n \quad \text{and} \quad \phi(f_n) = \phi_n = i \frac{2\pi}{M} \quad (3)$$

where M is the PSK modulation order (4 for Q-PSK, 8 for 8-PSK, etc.) and i can take any value between 0 and $M - 1$.

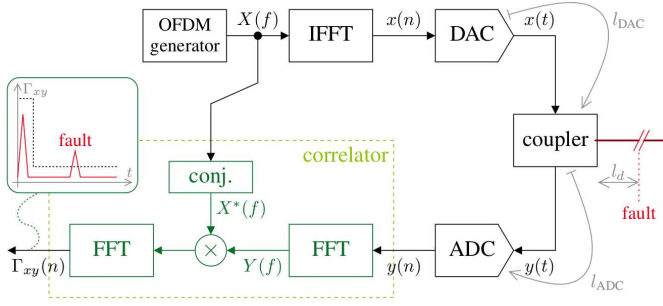


Fig. 1. Schematic diagram of a standard OMTDR based cable diagnosis system using a correlator between the injected signal $x(n)$ and the reflected signal $y(n)$ to detect the location of a fault in a CUT.

The time-domain discrete signal $x(n)$ is obtained by computing an Inverse Fast-Fourier Transform (IFFT) of the OFDM sub-carriers after-which a DAC generates $x(t)$, the analog domain version of $x(n)$. $x(t)$ is then injected to the coupler linking the DAC with the CUT and the ADC. The injected signal $x(t)$ travels over the CUT and reflects back once an impedance mismatch is met. In this case, the reflected signal $y(t)$ is sampled by the ADC to give rise to the discrete signal $y(n)$. The process of estimating the fault location is accomplished by a correlator block applying a cross-correlation function between the injected and reflected signal. In effect, the OMTDR reflectometry method is equivalent to an OFDM communicating system which determines a multi-path channel transfer function before data transmission.

Fig. 2 illustrates both the OMTDR signal spectrum $X(f_n)$ with f_n being the frequency of each orthogonal sub-carrier, and the frequency domain cable transfer function $H(f)$. In particular, $H(f)$ is computed as a function of the cable's attenuation function $H_C(f)$ and a phase function $\phi_C(f)$. Thereupon, the frequency domain received signal $Y(f_n)$ discretely defined for each sub-carrier of frequency f_n is defined as:

$$Y(f_n) = X(f_n)H_C(f_n)e^{-j\phi_C(f_n)} \quad (4)$$

Applying time-domain cross-correlation is reciprocal to frequency domain multiplication. Accordingly, the cable transfer function can be obtained by:

$$H(f_n) = Y(f_n)X^*(f_n) \quad (5)$$

$$= X(f_n)H_C(f_n)e^{-j\phi_C(f_n)}X^*(f_n) \quad (6)$$

$$= e^{j\phi_n}H_C(f_n)e^{-j\phi_C(f_n)}e^{-j\phi_n} \quad (7)$$

$$= H_C(f_n)e^{-j\phi_C(f_n)} \quad (8)$$

where $(*)$ is the conjugate of a complex value. The cable impulse response is then obtained by performing an IFFT to the resulting frequency domain cable transfer function $H(f)$. This impulse response is often referred to as reflectogram, and is used to localize and analyze each impedance discontinuity over the CUT. As a matter of fact, two properties usually determine the performance of a reflectometry system: the maximal distance to the fault l_{max} and the spatial resolution δ_l .

Starting with l_{max} , its value is a function of the number of sub-carriers, N , considered in the injected OMTDR signal:

$$l_{max} = \frac{v_g}{\Delta f_n} = \frac{N \cdot v_g}{f_s} \quad (9)$$

As seen in Fig. 2, Δf_n is the frequency difference between two adjacent sub-carriers, f_s is the DAC and ADC sampling frequency and v_g is the propagation speed on the cable. A trade-off between the desired l_{max} and N is done for each reflectometry system since the number of sub-carriers is directly related to FFT/IFFT sizes and consequently to the digital system complexity.

The spatial resolution δ_l refers to the accuracy of position of a fault along the CUT, as shown in Fig. 3. This property δ_l is defined as follows:

$$\delta_l = \frac{v_g}{2f_s} \quad (10)$$

In fact, maximizing δ_l is a major challenge for future reflectometry system implementations. Accordingly, the sampling frequency of the ADC and DAC should be incremented. Unfortunately, this will physically limit their accuracy and skyrocket their cost leading to a non practical system implementation from a commercial marketing aspect. Moreover, as described in eq. (9), increasing the sampling frequency reduces

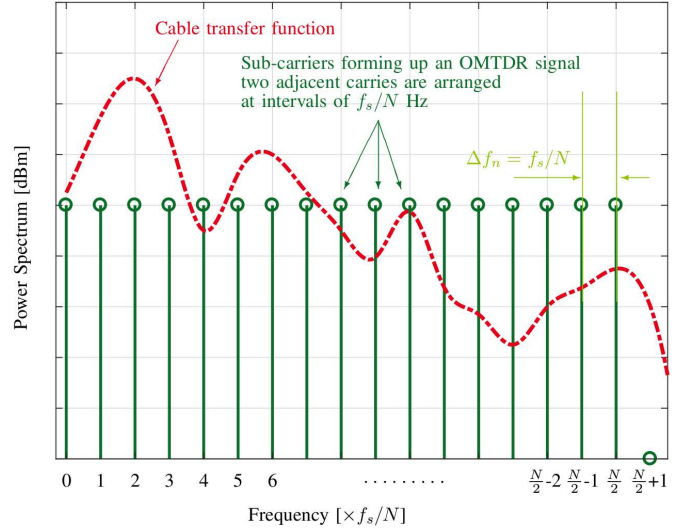


Fig. 2. Positive part of the spectrum of an OMTDR signal $X(f_n)$ with N sub-carriers (green) and the cable frequency-domain transfer function (red) defined in the same band as the multi-carrier signal, i.e., $f_s/2$ with f_s being the DAC sampling frequency.

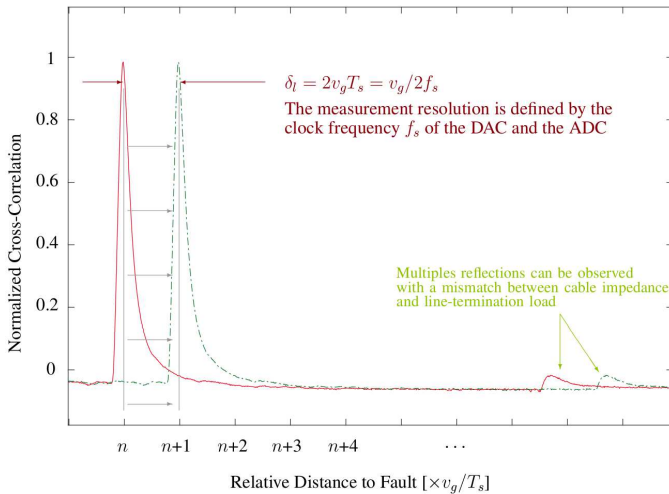


Fig. 3. Two reflectograms illustrating the spatial resolution δ_l . The measurement error of a fault location over a CUT is always lower than v_g/T_s meters, which is represented by the length difference between solid red and dashed-green reflectograms.

l_{\max} , and so is the range of the fault detection system for a given number of sub-carriers N . Following, we present three different techniques capable of efficiently bypassing the limits of δ_l without degrading the range or the material complexity.

III. METHODS FOR ENHANCING THE SPATIAL RESOLUTION

It has been explained in the previous section that increasing the sampling frequency of the ADC would improve the localization of any defect appearing on the CUT. However, more efficient conversion devices such as DAC or ADC become very expensive as either the resolution or the sampling frequencies increase. This section will present three promising methods for drastically improving the precision of the detection, using standard components for the ADC and also avoiding the use of clock-domain crossing mechanisms.

A. Incremental phase-offset shifting (IPS)

Because the periodically injected signal $x(t)$ is assumed to be stationary on the CUT, it is possible to rely on an incremental phase-offset shifting technique to sample the received signal at arbitrarily spaced time-stamps.

This method includes three main steps.

- 1) First, an acquisition and memorization of N values of $y(n)$ is performed from the ADC; sampled at $f_s = f_{\text{ADC}}$ with a reference phase-offset Φ .
- 2) Second, the delay of the sampling clock is incremented by a constant amount Δ_Φ after waiting for the sampling clock to be stabilized.
- 3) Third, the two previous steps are repeated Ω times, with Ω being the equivalent oversampling factor.

The system given in Fig. 4 depicts the implementation of the above-mentioned process. Specifically, the spatial resolution is

improved through a pseudo oversampling achieved by increasing the phase of the ADC sampling clock using a constant phase-shift. This is accomplished periodically by an acquisition period equivalent to $N \cdot T_{\text{ADC}}$ with $T_{\text{ADC}} = 1/f_{\text{ADC}}$.

Regrettably, the actual drawback of this method is that the time necessary to lock the clock phase loop between two shifts can be very long and depends on the technology used, such as the DLL (Delay Locked Loops) in FPGAs.

B. Using constant frequency offset (CFO)

This approach might recall Verniers method which is widely used in digital PLL for fine delay measurement between two pulses [29], but applied to a periodic time measurement with finite subdivision. Thus, the system is constructed so that the ADC sampling period (T_{ADC}) is spaced at a constant fraction of the fixed DAC period (T_{DAC}). The equivalent spatial resolution is equal to:

$$\delta_l = (T_{\text{DAC}} - T_{\text{ADC}}) \bmod T_{\text{DAC}}. \quad (11)$$

With Ω being the oversampling factor to be achieved, it is sufficient to increase T_{ADC} such that

$$T_{\text{ADC}} = T_{\text{DAC}} \cdot \frac{\Omega + 1}{\Omega}. \quad (12)$$

Fig. 5 presents the implementation of the aforementioned process. In fact, the spatial resolution is here improved through an oversampling period achieved using a constant time-shift between the emission clock instant and the sampling of the N sampling points, with $\Omega \cdot N \cdot T_{\text{ADC}}$ being the complete acquisition period.

The received signal is then acquired every T_{ADC} step so that each sample is interleaved

$$y(t + T_{\text{ADC}}) \mapsto y(n + 1) \mapsto y'((n + \Delta) \bmod [N \cdot \Omega]) \quad (13)$$

The register structure is a simple reordering buffer with a constant interleaving scheme. The write memory address is calculated with an adder of Δ modulo $N \cdot \Omega$ (i.e. the remainder of their Euclidean division) and the read memory address is simply incremental.

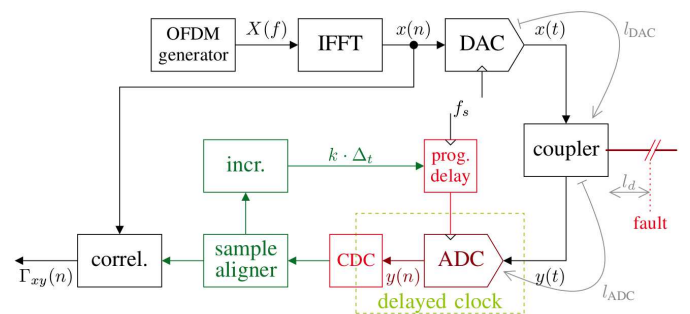


Fig. 4. Schematic diagram of an OMTDR based cable diagnosis system using a correlator between the injected signal $x(n)$ and the received signal $y(n)$ to detect the location of the fault. The acquisition resolution is improved through a pseudo oversampling achieved using a constant phase-shift increasing on the sampling clock of the ADC.

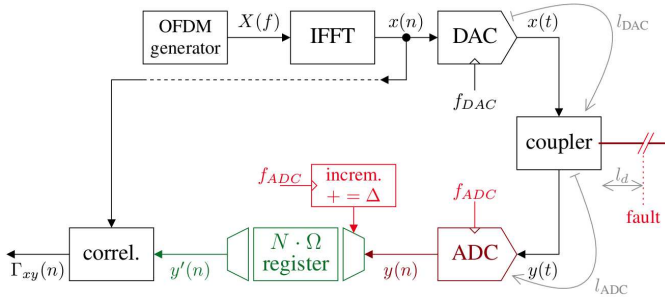


Fig. 5. Schematic diagram of a cable diagnosis system using a correlator between the injected signal $x(n)$ and the received signal $y(n)$ to detect the location of the fault. Acquisition resolution is improved through the reconstruction of the received signal which is sampled at f_{ADC} , a slightly different clock frequency than that of the DAC f_{DAC} .

Fig. 6 represents a simplified version of the oversampling method described in this section using a constant frequency offset between DAC and ADC, where a periodic signal $x(t)$ is sent through the CUT with four samples (A, B, C and D). The oversampling factor is hereby $\Omega = 4$ and $f_{ADC} = 4/5 \cdot f_{DAC}$.

Notably, the presented oversampling process does not require any high-performance DAC or ADC devices. As a result, less complex and lower cost systems are achieved. Furthermore, no complex mechanism is necessary to resynchronize burst-acquisitions between two phase-shifts as required by the DLL explained in the previous section. On the other hand, the presence of a given fault must be guaranteed for a given amount of time in order to finalize the recomposition procedure. In fact, interleaving the acquisition points produces a spreading of the signature of the fault over a longer acquisition period. Thus, the proposed approach might fail in detecting very short-duration (100 μ sec) intermittent hard faults in a CUT.

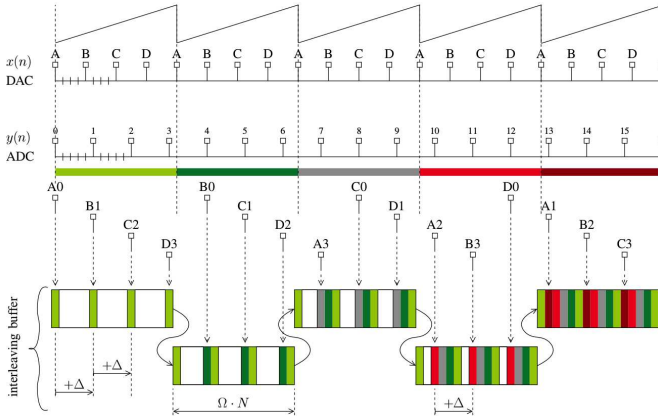


Fig. 6. Representation of a simplified version of the oversampling method using a constant frequency offset between DAC and ADC. The injected signal is composed of four samples (A, B, C and D). The oversampling factor $\Omega = 4$ and $f_{ADC} = 4/5 \cdot f_{DAC}$.

C. Analysis of the reflected signal phase (SPA)

The propagation of an electromagnetic signal through a CUT of length l has been defined in section II in the frequency domain as

$$Y(f) = X(f)H(f)e^{-j\phi(l,f)} \quad \text{with} \quad \phi(l,f) = \frac{2\pi fl}{v_g} \quad (14)$$

In the case of a system detecting and locating faults in a CUT by reflectometry, l is twice the distance to fault. This distance can be obtained by accurately controlling the frequency f of the injected signal with an a priori knowledge of v_g .

The arctangent of $Y(f)$ yields the phase $\phi(l,f)$ of the reflected signal propagating over a CUT of length l . Since the arctangent function returns a value within $[-\pi, \pi]$, $\phi(l,f)$ can be equal or greater than that of a frequency f_j , even if $f_i < f_j$. Consequently, the sought-after length l' is not the same for two different signals with frequencies f_i and f_j with a potentially wrong estimation for both cases. Getting a reliable estimation of the fault location on the CUT is possible by using multi-carrier signals. The multi-carrier signal spectrum $X(f)$ has been already defined in section II as a group of sub-carriers with different frequencies f_n having the following amplitude and phase functions:

$$|X(f_n)| = 1 \quad \forall f_n \quad \text{and} \quad \angle X(f_n) = \phi_n \quad (15)$$

Accordingly, the fault's location is relatively obtained from the difference between the phases of two adjacent sub-carriers given by:

$$\phi(l, f_{n+1}) - \phi(l, f_n) = 2\pi f_{n+1} \frac{l}{v_g} - 2\pi f_n \frac{l}{v_g}. \quad (16)$$

The fault's estimated location l' can thus be retrieved as:

$$l' = \frac{v_g \phi(l, f_{n+1}) - \phi(l, f_n)}{2\pi (f_{n+1} - f_n)} = \frac{v_g \Delta_\phi}{2\pi \Delta_f} \quad (17)$$

It is noteworthy that choosing the right frequency spacing between sub-carriers Δ_f is fundamental to avoid getting a phase difference $\Delta_\phi > 2\pi$. Accordingly, the maximum value of Δ_f can be obtained by fixing a maximum fault location:

$$\Delta_\phi = 2\pi \Delta_f \frac{l_{\max}}{v_g} < 2\pi \quad \implies \quad \Delta_f < \frac{v_g}{l_{\max}} \quad (18)$$

The system model describing the elements performing fault detection from multi-carrier phase analysis is presented in Fig. 7. As for OFDM, the time-domain multi-carrier signal is generated by a multi-carrier generator which sets the amplitude and phase of every single sub-carrier. An IFFT block then transforms the multi-carrier spectrum from the frequency domain into the time domain. Markedly, the DAC, ADC and the CUT are connected to each other.

In our case, the novel system is simplified by using a single FFT block rather than a complex correlator containing an FFT/IFFT block. The new FFT block feeds a phase computing block, relying on standard computing methods for trigonometric functions such as that described in [30]. Note that integer arithmetic (fixed point) operation is precise enough in the area of fault detection and diagnosis. The maximal value of Δ_f ,

defined in eq. (18), can be set by choosing the right number of sub-carriers, N , and the DAC sampling frequency f_s .

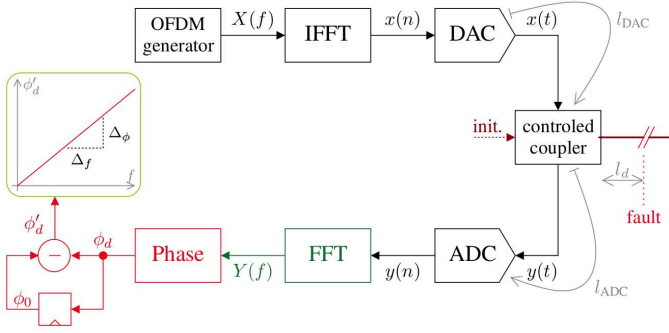


Fig. 7. Schematic diagram of the novel cable diagnosis system using a single FFT block to analyze the phase of the reflected reflectometry signal and locate a fault using the variation of the phase versus the variation of the frequency.

Locating a fault using the system presented in Fig. 7 is accomplished in three steps [31]. The first step begins by injecting the multi-carrier signal when the cable is still not connected to the reflectometry device (open-circuited). Connecting and disconnecting the cable can be autonomously done by implementing a switch. The phase $\phi_0(l, f_n)$ of the sub-carrier of frequency f_n with no cable coupled is defined as:

$$\phi_0(l, f_n) = \phi_n + \phi(l_{\text{DAC}} + l_{\text{ADC}}, f_n) \quad (19)$$

$$= \phi_n + 2\pi f_n \frac{l_{\text{DAC}} + l_{\text{ADC}}}{v_g} \quad (20)$$

where ϕ_n is the initial phase of the sub-carrier of frequency f_n as given by eq. (3), l_{DAC} is the path length between the DAC and the coupler, while l_{ADC} is the path length between the coupler and the ADC. Each value $\phi_0(l, f_n)$ is stored as a reference measurement in the register in front of the subtractor.

In the second step, the CUT is bounded to the coupler. The phase of the sub-carrier with frequency f_n for a cable with a fault at a distance l_d can be expressed as:

$$\begin{aligned} \phi_d(l, f_n) &= \phi_n + \phi(l_{\text{DAC}} + l_{\text{ADC}}, f_n) + \phi(2l_d, f_n) \\ &= \phi_n + 2\pi f_n \frac{l_{\text{DAC}} + l_{\text{ADC}}}{v_g} + 2\pi f_n \frac{2l_d}{v_g} \end{aligned} \quad (21)$$

The last step consists in subtracting from $\phi_d(l, f_n)$ the phases stored in the registering block in front of the subtractor:

$$\phi'_d(l, f_n) = \phi_0(l, f_n) - \phi_d(l, f_n) = 2\pi f_n \frac{2l_d}{v_g} \quad (22)$$

It is noteworthy that the part of the phase induced by the distances l_{DAC} and l_{ADC} has been avoided by applying a baselining approach where the a reference measure is subtracted from the CUT measure [15]. Accordingly, the only unknown parameter is l_d which can be estimated from eq. (17) by considering two adjacent frequencies. Furthermore, taking several adjacent sub-carriers permits a linear regression which yields a more precise estimate of the fault location. As a result, the phase value must be a continuous function, not bounded within $[-\pi, \pi]$. Such a

function is obtained by cumulating the phase values of previous frequencies. Thus, the phase value for a given frequency f_{n+M} and a given fault location l is as follows:

$$\phi'_c(l, f_{n+M}) = \sum_{i=0}^M \phi'_d(l, f_{n+i}) \quad (23)$$

It is important to point that the presented method has shown several advantages in an embedded system scenario when compared to standard OMTR methods based on OFDM signals. As a matter of fact, it is capable of cutting some frequency bands without any degradation in the diagnosis of the CUT. The autocorrelation function of an OFDM signal is strongly degraded when one or several carriers are attenuated, and so is the sensitivity and the precision of the OMTDR. This is not an issue for the phase analysis since the user chooses the first and last frequencies used to estimate the fault location.

The proposed method is expected to perform well in terms of resolution and fault location accuracy for point to point cables (easy known network configuration) whereas an increased complexity of the network (branches, junction, etc.) can alter the method's proper application. This is true as any ambiguity in the prior knowledge of the NUTs topology can lead to a total corruption of the phase function which can no longer be precisely calculated. Meanwhile, new methods integrating optimization algorithms with reflectometry methods have succeeded in unveiling the topology of any unknown wiring network [32]–[35]. Particularly, this can be a potential starting point towards testing complex networks with the proposed phase analysis method.

IV. RESULTS AND ANALYSIS

The performance of the SOMTDR reflectometry system recalled in section II and the aforementioned three proposed resolution enhancing methods are firstly evaluated by numerical simulations and then by experimental validation. More importantly, the last part of this section will present a performance analysis between all methods in terms of location accuracy and processing time.

A. Simulation results

In literature, the methods used to simulate and study the transmission line behavior are different. Most of them are obtained from the time dependent telegraphers equations modeled by RLCG circuit parameters for an infinitesimal length of the line. Notably, these equations describe the evolution of voltage and current as functions of the per unit length parameters demonstrated by the quantities R (resistance), L (inductance), C (capacitance) and G (conductance).

In our work, we opted to use the finite difference time domain (FDTD) method in an in-house solver developed under Matlab in order to solve telegraphers equations and model the signal propagation in a transmission line. Accordingly, the transfer function $h(t)$ of the CUT is simply obtained by integrating the cable's RLCG parameters in the developed model. These parameters may be theoretically calculated according to the physical and electrical properties of the transmission

line such as conductor diameter, material, stranding, etc [36], [37]. Otherwise, they may be estimated based on the channel response measurement accomplished by using a vector network analyzer (VNA) as we did in our case study [38], [39]. The coupler's transfer function has been determined from spice simulation.

A mixed domain model (analog and digital) has been also developed in order to quantify the performance in terms of spatial resolution of the proposed techniques. The analog domain is simulated from floating point reflectometry signals which are combined to the coupler and the CUT transfer functions. On the other hand, 10 bits fixed-point signals are used to model the digital domain, i.e. before the DAC and after the ADC. The reflectometry testing signal is an OMTDR signal composed from $N = 128$ sub-carriers. The oversampling factor considered for the IPS and CFO techniques described in section III-A and III-B is $\Omega = 16$.

To better illustrate the potential efficiency of the proposed methods, two different cable configurations have been considered. The first CUT model is a standard coaxial EN WZFRF15 cable with a propagation speed $v_g = 1.85 \cdot 10^8$ m/s while the other is a twisted pair MLB-24 cable used for general purpose aeronautical applications with $v_g = 1.44 \cdot 10^8$ m/s. Open-circuited hard faults were created at different distances from the coupling point with a 0.5 cm incremental distance step for both configurations.

The simulation results of the first cable are presented in Fig. 8 while those obtained for the MLB-24 cable are plotted in Fig. 9. As predicted, eq. (10) produces a spatial resolution of $\delta_l = 46$ cm and $\delta_l = 38$ cm while applying the standard OMTDR system for the first and second cable configurations respectively. Significantly, the resolution is highly enhanced with the stroboscopic sampling techniques presented by the standard IPS method in section III-A and the CFO technique detailed in sec. III-B, with δ_l considerably decreasing to a significant 3 cm and 2.4 cm respectively. More importantly, a notable accuracy is observed with the phase analysis based SPA technique of sec. III-C. Remarkably, an estimation error (lower than 1 cm) is obtained primarily due to the fixed-point operations done in the FFT and arctangent blocks.

B. Experimental validation

In order to check the feasibility of the proposed techniques for practical implementation, the reflectometry systems presented in Figs. 1, 4, 5 and 7 have been implemented in a specific architecture on a Field-Programmable Gate Array (FPGA) board driving a 10-bit DAC and a 10-bit ADC. Both converters are tightly coupled and connected to the same type coaxial cable considered in the numerical validation of the previous section. In fact, this architecture has been developed and designed to inject/receive OMTDR signals. The OMTDR-based reflectometry method is integrated in the FPGA chip developed at our lab as an electronic board under the name of "ArianneV2" presented in Fig.10. The injection/acquisition procedure to collect the OMTDR response is accomplished by a software developed in Python. This program performs the OMTDR-based communication via a Computer Unit (CU) by

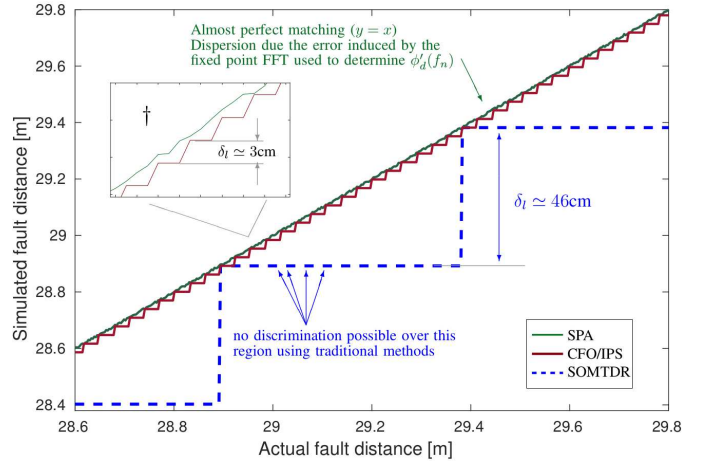


Fig. 8. Computational simulation comparison between the estimated and actual fault location for a standard coaxial EN WZFRF15 CUT after applying the SOMTDR correlation-based reflectometry and the IPS, CFO and SPA techniques. † Zoom around 29m.

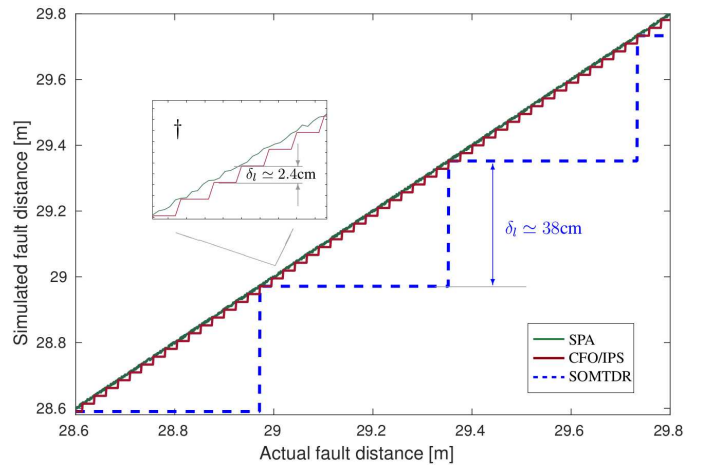


Fig. 9. Computational simulation comparison between the estimated and actual fault location for a twisted pair MLB-24 CUT after applying the SOMTDR correlation-based reflectometry and the IPS, CFO and SPA techniques. † Zoom around 29m.

either a wired or a wireless link. The whole experimental setup is demonstrated in the implementation of Fig. 10 where a wired connection was used between "ArianneV2" board and a standard state-of-the art computer. The DAC and ADC sampling frequency is set at $f_s = 188.8$ MHz. For the implementation of the method based on sampling frequency offset (section III-B), the ADC sampling frequency is $f_s = 177.7$ MHz, which produces an oversampling of $\Omega = 16$. The multi-carrier signal is an OFDM signal composed of $N = 128$ sub-carriers.

As specified by the OMTDR method, these sub-carriers are equally distributed in the interval from 0 to 188.8 MHz. The frequency spacing between the sub-carriers is around 1.475 MHz, which permits testing a cable ranging up to $l_{max} = 125.3$ m with a propagation speed $v_g = 1.85 \cdot 10^8$ m/s. It is important to note that this same propagation speed was

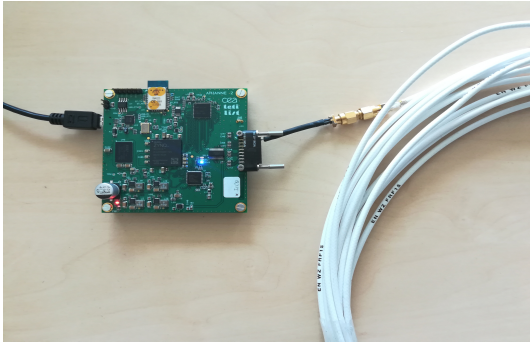


Fig. 10. The "ArianneV2" electronic board used for generating, injecting, receiving and processing the OMTDR signals into a CUT.

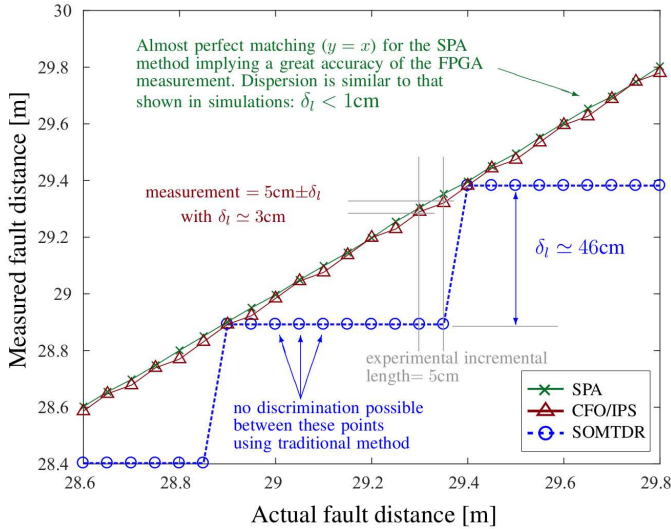


Fig. 11. Measurement of the fault location on an ArianneV2 platform versus the actual fault distance for a system applying the SOMTDR correlation-based reflectometry and the IPS, CFO and SPA techniques.

also used in the experimental post-processing validation.

Several acquisitions have been accomplished for different open-circuit fault locations along the CUT. The obtained results of the measurements are illustrated in Fig. 11 where each point represents an acquisition on the FPGA system. As seen on Fig. 11, the resolution of the estimation obtained by the standard OMTDR-based technique, i.e., the red line, is $\delta_l = 46$ cm. Significantly, the resolution described in eq. (10) is now greatly improved with the novel techniques proposed in section III. The dark red (triangle) line represents the standard IPS and novel CFO stroboscopic sampling techniques of sections III-A & III-B. Both techniques show an almost aligned similar spatial resolution of $\delta_l \simeq 3$ cm, which is consistent and agrees with the previous simulation results. On the other hand, a significant spatial resolution is obtained with the SPA technique of section III-C. As noted on Fig. 11, there is no actual discontinuity between two adjacent points using this novel method. The granularity reveals the repetition steps during our test-bench, where the faults have been produced

with a spacing of 5 cm. The maximum measured error at each point was $\delta_l < 1$ cm. More importantly, the actual spatial resolution of our new SPA method is finally a function of the digital quantification of the DAC and the ADC, the fixed point FFT and arctangent computing, in addition to the experimentally coupled noise.

C. Performance Analysis

In order to better quantify and understand the obtained results, a brief summary of the performance of each OMTDR-based reflectometry method investigated in this work is presented in Table I. Showing the lower average and maximal errors, the SPA technique is shown to be the best in terms of resolution and fault location accuracy. Notably, the technique has also proven to be faster in terms of acquisition time. On the other hand, the acquisition time of the CFO technique is more than 3 times faster than that of the standard IPS stroboscopic sampling method. For both techniques, the maximal and average measurement error is almost similar.

From another aspect, the SPA technique has shown to have degraded performance once complex branched networks are addressed. Thus, it couldn't be adapted for the diagnosis over complex cable topologies. In fact, this may be returned to the unstable phase pattern obtained in branched NUTs. Under this condition, the CFO method seems to be the most reliable in terms of accuracy and diagnoses latency.

TABLE I. PERFORMANCES COMPARISON FOR THE PRESENTED FAULT LOCATION METHODS

Method	Max. error [cm]	Average error [cm]	Acquisition time [ns]	Complex Topologies
SOMTDR	45.7	24.9	640	✓
IPS	3.2	1.7	39300	✓
CFO	3	1.5	11520	✓
SPA	<1	<1	640	✗

V. CONCLUSION

This paper investigated the potential of multi-carrier reflectometry, particularly the one based on OFDM signals (OMTDR), in precisely locating hard faults while resulting in a high spatial resolution. It has been shown that the performance of standard OMTDR based methods are greatly confronted by the physical limits attached to state-of-the-art converters. In fact, improving the spatial resolution while preserving highly accurate fault positions in short acquisition and processing periods necessitate outperforming ADCs and DACs. Accordingly, an increased complexity of hardware implementation and highly expensive elements are required.

To cope with these problems, we have recalled a standard method and proposed a novel one both based on the stroboscopic sampling technique: the IPS and the CFO respectively. Markedly, they have proven to be efficient in enhancing the spatial resolution and returning accurate estimates of fault's locations. Notably, they have also shown a significant over-sampling emulation while using standard traditional ADCs and DACs. Simulation and experimental results demonstrated

a remarkable ability of precisely locating hard faults with a maximum error at least 10 times lower than that afforded by standard OMTDR methods.

More importantly, we have also proposed a novel SPA method based on the analysis of the phase evolution of multi-carrier signals which is founded on the fact that the characteristics of a fault in a cable are specified in its phase information. Significantly, the obtained results have shown an implementation capacity using state-of-the-art material, which allows its integration into low cost and embedded hardware systems. Its surprising ability to locate faults in a maximum error of less than 1 cm is a milestone in the domain of fault detection and location techniques. Besides, the short acquisition time needed, less than 640 ns, makes its perfectly compatible for testing cables suffering from very short duration intermittent faults. On the other hand, its inconsistency with branched cable networks forms an interesting topic to be investigated in future work. More importantly, future work will need to deal with the performance and the robustness of the proposed methods in noisy environments.

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