



HAL
open science

CVD-synthesis and local characterization of TMDC monolayers for electronics

K Jaouen, Frédéric Lebon, T Croze, P Pedram, D Brunel, S Campidelli, B Jusselme, R Cornut, V Derycke

► To cite this version:

K Jaouen, Frédéric Lebon, T Croze, P Pedram, D Brunel, et al.. CVD-synthesis and local characterization of TMDC monolayers for electronics. Lavoisier Discussions: 2D materials on substrates, growth and properties, Jan 2020, Villard de Lans, France. cea-02438477

HAL Id: cea-02438477

<https://cea.hal.science/cea-02438477>

Submitted on 14 Jan 2020

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

CVD-synthesis and local characterization of TMDC monolayers for electronics

K. Jaouen¹, F. Lebon¹, T. Croze¹, P. Pedram², D. Brunel², S. Campidelli¹, B. Jousseme¹, R. Cornut¹, V. Derycke^{*,1}

¹LICSEN, NIMBE, CEA, CNRS, Université Paris-Saclay, CEA Saclay, F-91191 Gif-sur-Yvette

²GeePs, Université Paris-Saclay, 11 rue Joliot Curie, F-91192 Gif Sur Yvette

Abstract : Monolayers of transition metal dichalcogenides are very appealing for electronics, optoelectronics and photo/electro-catalysis. Despite the rapid progresses of synthesis methods, device performances remain very sensitive to defects and inhomogeneities (edges, second-layer islands, grain boundaries). Global characterization techniques such as mobility measurements in FETs or catalytic activity measurements by voltammetry cannot assess the impact of inhomogeneities. Local techniques (spectroscopy mapping, scanning probes...) are thus particularly important in the field of 2D materials.

In the group, we synthesize TMDCs monolayers by CVD and integrate them in devices, notably FETs with thin and robust molecular layers as efficient gate-dielectrics able to preserve mobility. In this presentation, I will first present our device-oriented motivations, our synthesis results and their characterization by conventional means. I will then present the development of other local techniques with the aim of illustrating their specific assets for the field of 2D materials. The BALM technique (backside absorbing layer microscopy), first developed as a high-contrast imaging technique, can be used to measure the complex refractive index of TMDCs or to detect surface contamination on monolayers with high sensitivity. Single-pass KPFM (kelvin-probe force microscopy) can map surface potential with high accuracy, and in particular monitor charge accumulation at grain boundaries. We notably use it on operating FETs. SECM (scanning electro-chemical microscopy), usually used to map electrochemical activity, can also be turned into a new contact-less method to measure 2D material conductivity and flake-flake contact resistance.

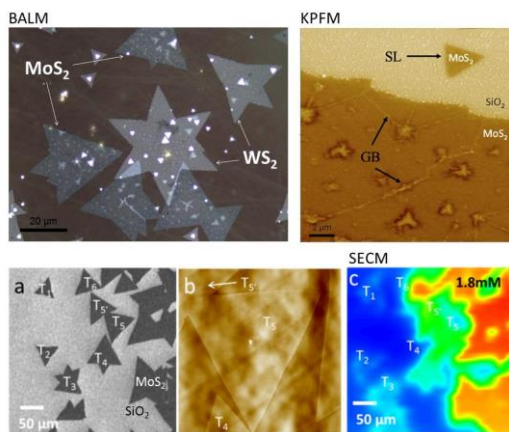


Fig. (top-left) BALM image of MoS₂ and WS₂ on gold; (top-right) single-pass KPFM surface potential map of MoS₂ monolayers; (bottom) conductivity measurement of MoS₂ monolayers by SECM (right) and same area first imaged by SEM and AFM (left).