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► **To cite this version:**

Jonas van Stappen, Chloé Dindault, Tiphaine Bourgeteau, Heejae Lee, Denis Tondelier, et al.. Low-temperature deposition of transparent conductive layers for perovskite-silicon tandem cells. SPIC 2017 (Science et Technologie des Systèmes pi-Conjugués), Oct 2017, Limoges, France. cea-02340816

**HAL Id: cea-02340816**

**<https://cea.hal.science/cea-02340816>**

Submitted on 31 Oct 2019

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## Low-temperature deposition of transparent conductive layers for perovskite-silicon tandem cells

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Since 2013, single-junction research-cell power conversion efficiencies of perovskite cells have risen by about 8%<sub>abs</sub> to 22.1%, while multicrystalline silicon and monocrystalline silicon efficiencies have risen by less than 2%<sub>abs</sub> and less than 1%<sub>abs</sub>, respectively [1]. Tandem cells with a perovskite top cell and a silicon bottom cell, recently achieving 23.6% in two-terminal configuration, present a promising alternative to further increase the relatively stagnant performance of already highly-optimized single-junction silicon cells [2]. To facilitate such an advance, methods to deposit high-quality transparent conductors (TCs) which do not subject the perovskite layer to degradation during deposition need to be found.

We utilize a spin-coating solution deposition process optimized for high reproducibility, yielding the single-junction MAPbI<sub>3-x</sub>Cl<sub>x</sub> perovskite cell stack shown in Figure 1, with an average efficiency of 7.94 ± 0.68 % with a non-transparent electrode. Two different TC electrodes are tested on these cells: evaporated thin-film semi-transparent Ag layers and low-temperature RF-sputtered indium tin oxide layers. For the latter, buffer layers of either thin-film Ag, Ag/BCP, Ag nanowires or interlinked PCBM are used to protect the organic layer stack from sputtering damage.

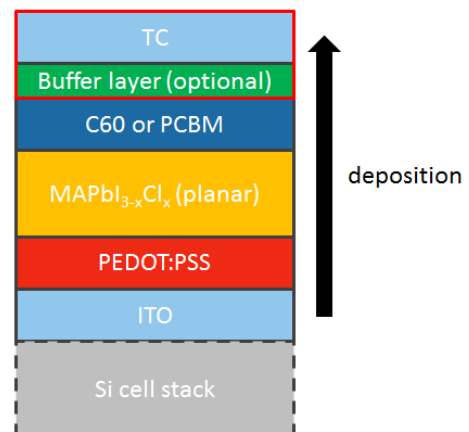


Figure 1 – MAPbI<sub>3-x</sub>Cl<sub>x</sub> cell stack used in this work, with the focus of this contribution highlighted by the red frame.

By comparing transparency and efficiency, we will identify the most suitable approach.

### References

- [1] M. A. Green, K. Emery, Y. Hishikawa, W. Warta, E. D. Dunlop, D. H. Levi and A. W. Y. Ho-Baillie, Solar cell efficiency tables (version 49). *Prog. Photovolt: Res. Appl.* (2017), 25: 3–13. doi: 10.1002/pip.2855
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