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Abstract #109351

Atomic Layer Deposition of Alumina on Silicon Nanotrees, Towards the Development of 3D Ultrastable Aqueous Si Microsupercapacitor

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Abstract Text:

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In recent years, significant attention has been paid to the development of micro-devices as innovative energy storage solutions. For instance micro-sensor networks such as sensors actuators or implantable medical devices require power densities and cyclability that are several orders of magnitude higher than those of conventional Lithium-Ion batteries. For such applications, Microsupercapacitors (MSCs), a developing novel class of micro/nanoscale power source are rising alternatives, and their integration “on-chip” could allow significant innovations to emerge.¹ Therefore, a great deal of attention has been focused on MSCs, for which large series of nanostructured active materials have been developed.

Following this trend, we have demonstrated through comprehensive investigations the interest of silicon nanostructures grown by Chemical Vapor Deposition (CVD) as electrodes materials for MSCs using ionic liquid electrolytes^{2,3}. The fine morphological tuning of the nanostructure allowed by the bottom-up approach enables specific designs of electrode architectures, with a considerable leeway compared to other techniques. Such latitude allows optimizing porosity and ionic and electronic pathways while keeping robust mechanical and thermal performances, depending on the target application. Nanostructures such as SiNWs and SiNTrs have displayed excellent electrochemical performances being stable over more than 1 million cycles of galvanostatic charge/discharge under a 4 V wide electrochemical windows in EMI-TFSI ionic liquid, with large power densities of 10 mW.cm⁻² and good capacitance values of 0.5 mF.cm⁻² at high current density of 0.5 mA.cm⁻².⁴

However a major silicon weakness which was still hindering its use with aqueous electrolytes is the native uncontrolled growth of silica when subjected to ambient atmosphere. Here we have developed a highly conformal passivation coating of a nanometric high-k dielectric layer of Al₂O₃ based on the rising Atomic Layer Deposition (ALD) technique. ALD has proven to allow a nanometric thickness control of the deposited layer while being highly conformal and covering. Electrochemical stability performances in ionic liquid, were enhanced allowing symmetric 2 electrode devices to reach an unprecedented cell voltage of 5.5 V⁵, improving energy and maximum power densities compared to pristine nanostructured silicon. The cyclability was also largely enhanced, with only 3% capacitance fade after 10⁶ galvanostatic charge/discharge cycles at 4 V, and no degradation even after several 10⁵ resilience cycles over a 5 V window⁵. Moreover, the protective alumina layer enables the use of aqueous electrolytes for nanostructured Si based MSCs, which significantly increases the specific power of the devices up to 200 mW.cm⁻² at 0.5 mA.cm⁻² while keeping the capacitance performances at 0.5 mF.cm⁻². Furthermore the system is remarkably able to retain 99% of its initial capacitance after 2 billion galvanostatic charge-discharge cycles at high current density of 0.5 mA.cm⁻² in an aqueous electrolyte of Na₂SO₄.

Eventually we have investigated the pseudocapacitive response of such MSCs in aqueous electrolytes by a simple drop-cast method of a PEDOT-PSS film. The device exhibited promising performances with a specific energy of 2 Wh.kg⁻¹ and a power density of 300 W.kg⁻¹ at a current density of 1 A.g⁻¹. The MSCs was able to retain 80% its initial capacitance after 50,000 galvanostatic charge-discharge cycles at 0.5 A.g⁻¹.

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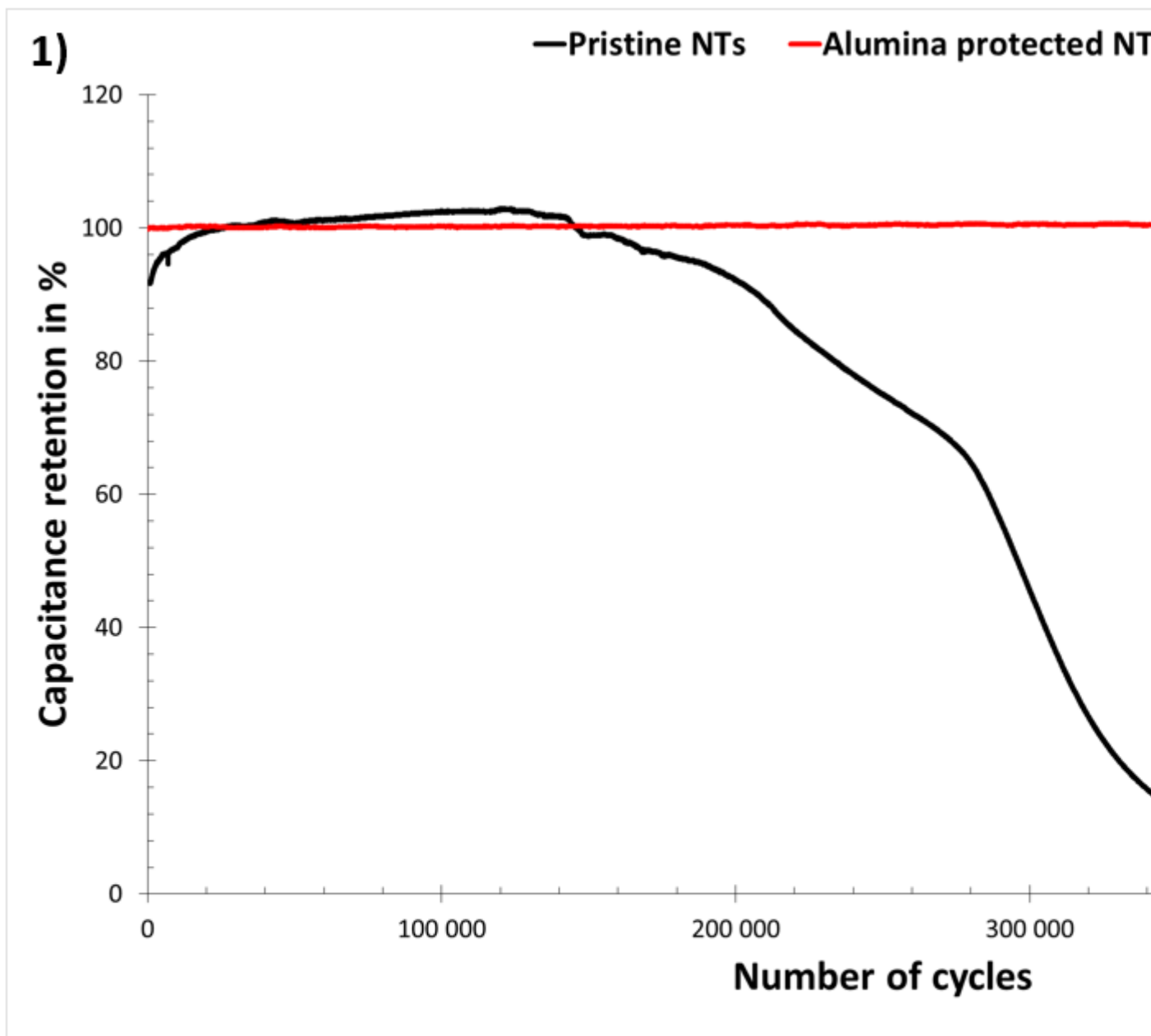


Fig. 1 Cycling performances of Alumina protected Si NTs in Na_2SO_4 at a current density of 0.5 mA cm^{-2}

