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Research Report

InKS, a programming model to decouple performance from semantics in simulation codes

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Abstract—Existing programming models lead to a tight interleaving of semantics and computer optimization concerns in high-performance simulation codes. With the increasing complexity and heterogeneity of super-computers this requires scientists to become experts in both the simulated domain and the optimization process and makes the code difficult to maintain and port to new architectures. This report proposes InKS, a programming model that aims to improve the situation by decoupling semantics and optimizations in code so as to ease the collaboration between domain scientists and expert of high-performance optimizations. We define the InKS language that enables developers to describe the semantic of a simulation code with no concern for performance. We describe the implementation of a compiler able to automatically execute this InKS code without making any explicit execution choice. We also describe a method to manually specify these choices to reach high-performance. Our preliminary evaluation on a 3D heat equation solver demonstrates the feasibility of the automatic approach as well as the ability to specify complex optimizations while not altering the semantic part. It shows promising performance where two distinct specifications of optimization choices in InKS offer similar performance as existing hand-tailored versions of the solver.

Keywords—HPC, programming model, separation of concerns.

I. INTRODUCTION

It is more and more common to identify simulation as the “third pillar of science”[13] together with theory and experimentation. Parallel computers, sometimes heterogeneous (e.g. GPGPU), provide the computing power required by the more demanding of these simulations. The complexity of these architectures do however force scientists to write complex code (using vectorization, parallelization, etc.) to take advantage of them. Since these optimizations depend on the targeted machine, they have to be adapted whenever the code is ported to a new architecture. Existing programming models do unfortunately lead to a tight interleaving of semantics and optimization concerns. It forces developers to become experts of both the simulated domain and computer optimizations and makes it difficult to maintain a code targeting multiple distinct architectures.

Many approaches have been proposed to improve this situation in the form of libraries or languages. Amongst those, many do however still mix computations and optimizations while others restrict the range of optimizations that can be implemented.

In this report, we propose the independent kernel scheduling (InKS) programming model that aims to separate semantics from optimization in high-performance simulation codes. It offers a language based on C where domain scientists can express the semantic of the code with no concern for performance. We describe the implementation of a compiler able to automatically execute this code to test the validity of the semantic. We also support the specification of execution choices in a distinct file to obtain an optimized version.

The remaining of the report is organized as follows. Section II identifies semantics and optimization aspects on a simple example to determine the requirement for a model to separate them and Section III presents and discusses related work. Section IV defines the InKS programming model and its implementation while Section V evaluates the approach. Section VI concludes and presents some perspectives.

II. ANALYSIS

In this section, we categorize the various aspects interleaved in simulation codes as either semantics or execution choices. We do so by analyzing multiple optimized implementations of a 7 points finite difference method 3D heat equation solver described in [1]. Listing 1 shows the simplest of those implementations, based on a double buffering strategy. Another implementation provides cache blocking over two of the three space dimensions that can be specifically tuned for the machine cache size. A third implementation provides cache blocking over two of the three space dimensions that can be specifically tuned for the machine cache size. A third implementation uses recursive function calls to implement a cache oblivious method with implicit blocking in four dimensions (3 in space, plus time).

In these three examples, linearized arrays (lines 4 and 5 of Listing 1) store the temperature values. The Index3D macro (line 1 and 2) maps from the 3D space coordinate of the mesh to the linear memory space. The time coordinate accessible in
All studied implementations of the heat equation solver use loops. The content of these loops is made of computations that operate on the arrays content (lines 11 to 18 of Listing 1) and is very similar from one version of the code to the other part from indexing issues previously discussed. The control part of the loops that gives values to indexes and schedules computations inside the loops on the other hand differs from one implementation to the other. In the example from Listing 1, the loops iterate in a pretty straightforward order whereas those from Listing 3 used for cache blocking are more complex. In the cache oblivious examples, the loops are different again and the iterations depend on parameters of the recursive function calls. These schedules are different to improve cache behavior, but all respect ordering constraints, namely that any value has to be written to memory before it is first read and that its storage space in memory must not be reused for another value before it is last read. The content of the loops as well as these ordering constraints thus constitute the semantic part while the choice of a specific schedule that respects the constraints is an optimization choice.

All the examples end with the values from the target time-step in the A0 array. Among all the computed values, we can consider this subset as the result of the program. This also constitutes a part of the application semantic.

To summarize, we have identified four concerns that form the semantics of the 3D heat equation solver: the values that exist during the execution, the computations done inside the loops, the constraints on computation order and the target result. We also have identified two types of execution choices: the memory mapping of data and the specific scheduling to use. More optimization choices could appear, for example in distributed memory parallel versions of the code where choices related to the distribution of data on nodes and communications would have to be made. If the semantic part contains enough information to derive a sequential version of the code however, there is no reason for a parallel version to require more information.

All in all, these examples written in C interleave semantics and optimization choices. A complete new code is written to demonstrate each distinct optimization. In order to ease the collaboration between specialists of the simulated domain and specialists of computer optimizations, one would like a programming model that clearly separates these two aspects. Even if this is an important goal, this should however not come at the cost of a much increased complexity for the specification of the semantic and it should remain possible to test this semantic without having to specify complex optimization choices. It should also be possible to specify any optimization and the specification of these choices should not be much more complex than it currently is in existing imperative languages. It should be possible to express a wide range of different problems in this language so as to cover as many simulation domains as possible and to include this inside another program written in existing languages such as C or Fortran for example to make the progressive adoption of the language possible. The following section presents and discusses existing programming languages with related goals.
Multiple approaches have been proposed to ease the development of high-performance simulation code. Some approaches aim to ease the implementation of execution choices. For example, OpenMP [6] eases writing shared memory parallel code with features such as the specification of independent loop iterations that can be executed in parallel. Partitioned global address space (PGAS) languages such as Co-Array Fortran [14], UPC [11], X10 [7] or Xca...
Data is represented using logically infinite multidimensional arrays that have no direct link with memory. Arrays are declared using the \#pragma inks declare notation. These are dynamic single assignment arrays where each coordinate can only be written once but read as many times as required. For example, in line 6 of Listing 4, a four dimensional (3D space + time) array named Heat is declared to store the temperature values as it evolves during the simulation.

Computations are specified as C++ code enclosed in a function template: a kernel. All logical arrays accessed are taken as parameter of the function and the type of the array is a template parameter. The function also takes coordinates as parameters to access values in arrays. The array type passed to the function supports the use of the parenthesis operator for multi-dimensional indexing. For example, the Inner kernel in line 17 of Listing 4 uses the Heat array and the coordinates x, y, z and t to compute the stencil. On the other hand, the StencilInit kernel in line 27 does not receive any coordinate because it applies to a fixed location of the array taken as parameter: the values at t = 0.

Constraints on execution order are based on data dependencies of the kernel. Dependencies are defined using the \#pragma inks <kernel> in ... out ... notation. They uses the coordinate system to declare data that is used in input or created in output of a kernel. For example, lines 8 to 12 of Listing 4 specify that the Inner kernel accesses 7 points as input and generate the value of one as output. The dependencies of the StencilInit kernel in lines 24 and 25 express the lack of input using the keyword null and the generation of every values in the first three dimensions using the star (*).

Dependencies are not enough to fully specify execution order. Indeed, both the kernels StencilInit and Inner could be used to generate the values at t = 0 given this information only. The coordinates for which a kernel is valid (its evolution domain) thus also have to be specified. They are specified using the \#pragma inks <kernel> definition notation. The bounds of these sets are expressions that can combine direct constants and constants specified using \#define. The lower bound is included and the upper one excluded. For example, the evolution domain of the Inner kernel in lines 13 and 15 of Listing 4 limits the values at t = 0 from being considered. The StencilInit kernel on the other hand uses no coordinate and does thus not need an evolution domain.

The target result of the program is a subset of the declared data. This is specified using the \#pragma inks target notation. For example in line 31 of Listing 4 the set of all values at the last iteration is selected by using the star notation for space coordinate and a single value for the time coordinate.

These elements constitute the InKS language and support the expression of all elements of semantics identified in Section 1. Let us now roughly demonstrate that this constitute the whole semantic of a program; i.e. that the language carries enough information to execute it.

B. InKS language completeness analysis

The informations available in the InKS language are:

• the set of logical arrays \( A = \{a_1, ..., a_n\} \)
• the dimension \( \dim(a) \in \mathbb{N} \) of each logical array \( a \in A \)
• the set of kernels \( K = \{k_1, ..., k_n\} \)
• the definition domain \( \text{dom}(k) \) of each kernel \( k \in K \) with its dimension \( n = \dim(k) \subseteq \mathbb{Z}^n \)
• the dependencies (inputs \( I_k \) and outputs \( O_k \)) of each kernel \( k \) in the logical arrays
• the set of targets \( T = \{t_1, ..., t_n\} \).

We call kernel instance the association of a kernel \( k \) with a coordinate from its domain \( \text{dom}(k) \subseteq \mathbb{Z}^n \) and we denote \( K \) the set of all kernel instances.
at its dependencies and we can define $\leq$ the transitive closure of $<$ that constitute a partial order relation on $K$. The set of kernel instances $K_x$ that must be executed to generate $T$ is the set of all instances that come before at least one instance in $K_T$.

$$K_x = \{ k \in K | \exists k_t \in K_T, k \leq k_t \}$$

The data $D_x$ that will have to be allocated at some point for the execution of all $K_x$ is the data that is the input or output of at least one such kernel instance. For the program to be valid, all the data instances accessed as input of an executed kernel instance must be part of the output of another kernel instance, one can therefore define the allocated data from the output only.

$$D_x = \bigcup_{k \in K_x} O(k) - I(k)$$

One can augment the executed kernel instances with instances representing the allocation $ka(d)$ and deallocation $kd(d)$ of each data instance $d$. The order relation can also be defined on these kernel instances by taking into account that a data instance must be allocated before it written and deallocated after it is last accessed.

$$K^+_x = K_x \cup \bigcup_{d \in D_x} \{ ka(d), kd(d) \}$$

$$\forall (k, d) \in K_x \times D_x, \ d \in I(k) \iff ka(d) \prec k$$

$$\forall (k, d) \in K_x \times D_x, \ d \in O(k) \iff k \prec kd(d)$$

To summarize, the InKS language provides enough information to construct the $K^+_x$ set that specifies all memory allocations and deallocations and kernel instances to execute with a partial order relation that determines the constraint on the scheduling of these operations. Let us now describes the actual implementation of this process in the InKS compiler.

C. Source-to-source compiler

A naive approach for the implementation of the scheduling could consist in enumerating the set of operations to execute and to generate a source code that does each of them in a valid order. The complexity of such an approach—both in time and space— as well as the generated code size would however be at least linear with the number of instances, i.e. the problem size. In fact, if the kernels implemented by the user operate at a fine enough grain, the compilation could easily require more resources than the execution itself. This situation would not be acceptable and we have to rely on a more compact representation of the problem and generated code.

The polyhedral theory offers a solution in the case where each domain can be described in terms of polyhedron. Given the language introduced in Section IV this is the case since we currently restrict ourselves to unions of rectangle regions. We have therefore chosen to base our implementation on the ISL [16] library that supports the manipulation of sets and relations of integer points bounded by linear constraints.

$$k \in K : \text{dom}(k) \mapsto K$$

$$K = \bigcup_{k \in K} \left( \bigcup_{t \in \text{dom}(k)} k(t) \right)$$

Similarly, we call data instance the association of a logical array $a$ with a coordinate from its domain $Z^{\text{dim}(a)}$ and we denote $D$ the set of all data instances.

$$a \in A : Z^{\text{dim}(a)} \mapsto D$$

$$D = \bigcup_{a \in A} \left( \bigcup_{t \in Z^{\text{dim}(a)}} a(t) \right)$$

The inputs $I_k$ and outputs $O_k$ dependencies of a kernel $k$ map each instance of this kernel to the data it reads or writes. We denote $I$ and $O$ the general input and output relations formed as the union of all kernel dependencies that map kernel instances to data.

$$I_k, O_k : K \mapsto \mathcal{P}(D)$$

$$I = \bigcup_{k \in K} I_k, O = \bigcup_{k \in K} O_k$$

In order for the program to be well formed, a given data instance should only be produced by a single kernel instance. That is, the intersection of the output relation applied to two distinct kernel instances should always be empty. On the other hand, multiple kernel instances can take the same data instance as input.

$$\forall k_1, k_2 \in K, \ k_1 \neq k_2 \Rightarrow O(k_1) \cap O(k_2) = \emptyset$$

Each target $t$ is a subset of the data that the computation must generate, we denote $T$ the union of all targets.

$$t \in T : t \subseteq D$$

$$T \subseteq D = \bigcup_{t \in T} t$$

The goal of the compiler is to generate a code that computes $T$. Consequently, for the program to be well formed, $T$ must be the output of some kernel. We denote $K_T$ this set of kernel instances that generate the target data.

$$K_T \subseteq K, \left( \bigcup_{k \in K_T} O(k) \subseteq T \right) \land (k_1 \in K_T \Rightarrow O(k) \cap T \neq \emptyset)$$

In addition, a kernel instance $k_1$ must be computed before an instance $k_2$, denoted $k_1 \prec k_2$ if $k_1$ generates data as output that $k_2$ accesses as input.

$$\forall k_1, k_2 \in K, \ O(k_1) \cap I(k_2) \neq \emptyset \iff k_1 \prec k_2$$

For the program to be well defined, there must be no loop in its dependencies and we can define $\leq$ the transitive closure of $\prec$ that constitute a partial order relation on $K$. The set of kernel instances $K_x$ that must be executed to generate $T$ is the set of all instances that come before at least one instance in $K_T$.

$$K_x = \{ k \in K | \exists k_t \in K_T, k \leq k_t \}$$

The data $D_x$ that will have to be allocated at some point for the execution of all $K_x$ is the data that is the input or output of at least one such kernel instance. For the program to be valid, all the data instances accessed as input of an executed kernel instance must be part of the output of another kernel instance, one can therefore define the allocated data from the output only.

$$D_x = \bigcup_{k \in K_x} O(k) - I(k)$$

One can augment the executed kernel instances with instances representing the allocation $ka(d)$ and deallocation $kd(d)$ of each data instance $d$. The order relation can also be defined on these kernel instances by taking into account that a data instance must be allocated before it written and deallocated after it is last accessed.

$$K^+_x = K_x \cup \bigcup_{d \in D_x} \{ ka(d), kd(d) \}$$

$$\forall (k, d) \in K_x \times D_x, \ d \in I(k) \iff ka(d) \prec k$$

$$\forall (k, d) \in K_x \times D_x, \ d \in O(k) \iff k \prec kd(d)$$

To summarize, the InKS language provides enough information to construct the $K^+_x$ set that specifies all memory allocations and deallocations and kernel instances to execute with a partial order relation that determines the constraint on the scheduling of these operations. Let us now describes the actual implementation of this process in the InKS compiler.
ISL provides functions to allocate multidimensional spaces in which one can create multidimensional sets. These sets – potentially parametric– contain every integer point between the set bounds. One can also manipulate these sets with different operations (i.e. projection, intersection, union, etc.). One also can create relations from the spaces or create them to link two sets. There are also many operations that involve relations or operations (i.e. projection, intersection, union, etc.).

One of the possible use of ISL is for dependency analysis and it leads to many applications, such as loop optimizations [3]. In our case, we use ISL to compute a valid scheduling of the required kernel instances. We therefore define sets using ISL that corresponds to the kernel and data instances. This vision enables the compiler to compute dependencies directly on sets of kernel instances instead of individual instances and makes the complexity depend on the number of such sets instead of the number of instances in them. Eventually, we generate the code to schedule the execution as loops which largely reduces the size of the code compared to an approach where each kernel instance would be explicitly executed.

The dependency analysis relies on a four steps algorithm:
1) creation of the spaces and sets that represent the data and kernel domains;
2) creation of the relation between kernels and data instances;
3) creation of the order relations between kernels;
4) computation of the kernel instances that need to be executed to compute the targets.

### Algorithm 1: Sets creation step

First, the InKS compiler creates a space for each logical array and kernel depending on its dimension and a creates a set in each kernel space bounded by the kernel evolution domain. This is summarized in Algorithm [1]

Then, the compiler creates the input and output relations. For each kernel, we generate as many input relations as it requires values and as many output relations as it generates values. Input relations go from the data space to kernel space and opposite for output relations with constraints added to match the dependencies specified in InKS. An example of the constraints generated for the code presented in Listing [5] is given in Figure [1]. For each relation, the compiler also creates a set that represents the data concerned by the relation. This is summarized in Algorithm [2].

The third step computes the order relation for kernel instances. A relation exists between two kernels $k_1$ and $k_2$ when the output of $k_1$ is required as input of $k_2$. Using isl, the compiler computes the intersection of the input set for each kernel input relation with the output set of each kernel output relation. If the intersection $\cap_i$ is not empty a dependency relation is generated as the composition of the inverse of the output relation and the input relation. This enables us to generate $R_{\text{map}}$ as the union of all such relations that the orders all kernel instances.

The last step computes the kernel instances the program must actually execute. The compiler applies the inverse of the output relation to the target data set to determine the set of kernel instances that generate the target. Then it inverts the $R_{\text{map}}$ order relation and computes its transitive closure. The resulting relation is applied to the set of kernel instances that generate the target to determine the complete set of instances to execute.

ISL propose a feature to generate a schedule that passes through all the points of a set by respecting an order relation. We use this feature to generate a schedule that iterates over

```plaintext
#pragma inks Inner \  
\in H(x, y, z, t-1) \  
\out H(x, y, z, t) \  
template <typename T> \  
void Inner(T & H, int x, int y, int z, int t) \  
{ \  
    //... \  
}
```

Listing 5. Example of input and output dependencies in InKS code
the kernel instances that must be executed in a valid order. First, the compiler creates a schedule constraint using the kernel instances set and the order relation. Then it creates a schedule from this schedule constraint. ISL also supports the conversion of this schedule into an abstract syntactic tree (AST) that the compiler then transforms into valid C++ loop nests. The generated code is valid C++ that can be called from any other C++ code. For example for the 3D heat solver InKS semantic presented in Listing 4, the compiler generates the code presented in Listing 6.

Regarding logical arrays memory mapping and allocation, an efficient solution can not only take into account dependencies for allocation and deallocation of data. Allocating memory at the point granularity would lead to huge overheads as do irregular memory accesses. This aspect is still a work in progress in our compiler and the current implementation allocates all memory before starting to execute kernels and deallocates it after all executions. The mapping uses a distinct rectangle line major allocation for each logical array and makes no provision for memory reuse at all. It is implemented by instantiating a dedicated InKSArray class locally to let the C++ constructor/destructor mechanism handle memory management. This naive approach leads to extreme memory consumption that depends on the product of all dimensions sizes (including time) and offer very bad performance. It does however make it possible to test the other aspects of the compiler.

D. Manual specification of execution choices in InKS

```cpp
#include "inks_file.cpp"  //Contains inks code
#include "InKSArray.h"  //Ease code writing

//Overload parenthesis operator
InKSArray Heat(nx, ny, nz, 2);
Stencillnit(Heat);

//Ease code writing
for(int t=1; t<timesteps; t++){
  for(int y=1; y<ny-1; y++){
    for(int x=1; x<nx-1; x++){
      for(int z=1; z<nz-1; z++){
        for(int c0=0; c0<timesteps; c0++)
          Heat.swap();
      }
    }
  }
}
```

Listing 7. Naive version using InKS manual approach

The manual specification of execution choices consist in implementing code equivalent to that of Listing 6. The developer must provide classes that overload the parenthesis operator for the arrays and loop nests that schedule kernels execution in a valid order. The kernels can however be seen as black boxes whose understanding is not required. This code can include the semantic part of the code so as to inline function calls and offer similar performance as if it was written in a single file.

Listings 7 and 8 show examples of execution choices implementing the naive and cache oblivious optimizations respectively.
InKS, we compare our implementation with the 3D heat equation solver implementations discussed along the report [11]. We have implemented the same solver using InKS. The semantic code is the one presented in Listing 4. We have implemented two versions of optimization choices: the naive version (Listing 7) matches the simplest version from the reference and the cache oblivious version matched the similarly named one from the reference. We have evaluated the complexity of the generated code using the GNU complexity tool that generates a score based on cyclomatic complexity. For performance evaluations, we have compiled all codes using the GCC 4.7.2 compiler with the -O3 flag and executed the result on a single core of an Intel Xeon E5-2670 node with 32 GB of RAM. Both aspects are presented in Table I.

This evaluation based on a single solver can of course not be used to discuss the applicability of InKS to a wide range of simulation domains. As a matter of fact, the InKS language is most likely not even Turing complete as it does for example not support the expression of unbounded loops (such as those used when iterating until convergence for example). The use of InKS for unstructured of adaptive meshes is also something that has to be explored. However, while the language has some technical limitations that will be explored in future work, it does not make any assumption regarding the simulated domain and offers abstractions that are close to the computer rather than from a specific domain. This enables us to be confident regarding its adaptability to multiple domains.

As it was the main target of this work, InKS supports the specification of semantics and optimization choices in completely distinct sets of files. All elements identified in Section II pertain to the right file. and two distinct optimization choices: the naive vs. cache oblivious have been implemented for a single semantic file. It thus offers a clear separation of concerns between these aspects.

We have not found any pertinent metric to evaluate the simplicity of writing the semantic code. The code presented in Listing 4 is however very close to the most naive implementation possible where loops are replaced by evolution domains and access descriptors for kernels. The difficulty to write such a code can thus be compared to writing the most naive code possible where one does for example not have to worry about iteration orders. The situation could be improved even further by automatically detecting access descriptors when possible. Syntactic sugar could also be added to simplify expressing some recurrent patterns such as reductions or equality kernels.

The InKS compiler supports the automatic execution of this code. The compilation takes in the order of the second to generate the code presented in Listing 6. Execution is currently very inefficient and is nearly 40 times slower than the naive approach. It also consume much more memory that depends on the number of time-steps where manual implementations do not. Preliminary tests have however shown that solving the memory problem should also largely improve the execution complexity.
time problem as discussed in Section [IV].

Both the automatic and manually optimized versions of the code are standard C++ code that offer a C compatible interface. This code can easily be called from a larger program implemented in a language that supports the C calling convention. This is the case of most existing languages including C, C++, Fortran but even also python or Julia for example.

The specification of optimization choices is very close to their expression in C for example. The GNU complexity score is slightly lower due to the fact that the optimization code does not include the core of the kernels. The fact that an already existing language is used means that specialists of optimizations can reuse their knowledge in InKS. The existence of a reference code automatically generated from the semantic only also makes it possible to easily test the optimized code validity. In addition, in future work, information from the InKS annotations of the semantic could be used to automatically generate code representing common optimization patterns that the specialist could use.

Finally, when it comes to performance, the approach makes it possible to express optimizations that does not change the semantic. Optimizations such as changing the numerical scheme or changing the order of operations, potentially making the code generate different results is however out of the scope of this work. In the case of the 3D heat equation solver, both the naive and cache oblivious versions were trivial to implement and their performance match (or slightly outperform) the reference performance.

VI. CONCLUSION AND PERSPECTIVES

This report has presented InKS, a programming model intended to decouple optimization choices from semantics in high-performance simulation codes. InKS offers a language based on C++ and pragma annotations to express the semantics of simulations and supports automatic execution as well as manual specification of execution choices. Preliminary evaluations show that InKS manages to completely separate semantics from execution choices while enabling any optimization that can be expressed in C++ to be used. They also show that with manual choice of optimizations, InKS performs similarly to hand-tailored code in a more classical language while the automatic execution does not currently offer very good performance. One should however note that this automatic execution is only intended to test the semantic of the code and that the expression of both the semantic and optimization choices is typically no more complex than with an imperative language like C.

Many directions for future work have been identified in this report. The first such work we plan to tackle is the improvement of the memory management in the InKS automatic compiler so that the generated code does not differ by orders of magnitude in terms of performance from a typical implementation. A maybe more interesting feature is to support tools to ease the expression of optimizations. Many of the runtimes and languages identified in Sections [III], such as OpenMP, Kokkos or StarPU could be wrapped to ease their use in optimization. By generating code that uses information from the InKS pragma annotations, a lot of tedious code could be automatically generated, thus easing the work of the developer.

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