

DE LA RECHERCHE À L'INDUSTRIE



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# A First DFA on PRIDE: from Theory to Practice

Works presentation at CRiSIS 2016

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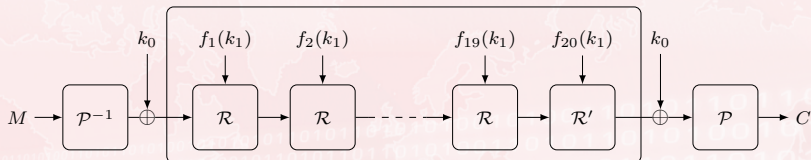
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- ① **The PRIDE block cipher**
  - The structure of PRIDE
  - The PRIDE round function
- ② **Differential Fault Analysis of PRIDE**
  - General principle
  - Differential properties of the PRIDE S-box
  - Properties that make the attack effective
- ③ **Practical implementation of the DFA on PRIDE**
  - Implementation of the device
  - Exploitation of obtained faults
- ④ **Countermeasures**
  - Duplication of computations
  - Desynchronization
  - Masking
- ⑤ **Conclusion and perspectives**

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### ■ The structure of PRIDE

Iterative block cipher composed of 20 rounds and introduced by Albrecht & al. in 2014. It takes as input a 64-bit block and uses a 128-bit key  $k = k_0 || k_1$ .



### The key scheduling

We denote  $k_{1_i}$  the  $i$ -th byte of  $k_1$  then

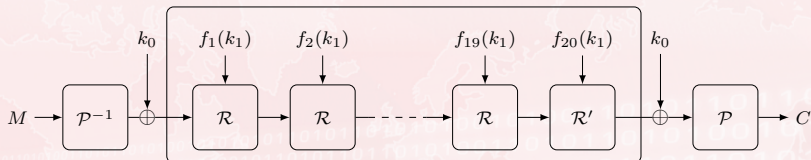
$$f_r(k_1) = k_{1_0} || g_r^{(0)}(k_{1_1}) || k_{1_2} || g_r^{(1)}(k_{1_3}) || k_{1_4} || g_r^{(2)}(k_{1_5}) || k_{1_6} || g_r^{(3)}(k_{1_7})$$

for round  $r$  with

$$g_r^{(i)}(x) = (x + C_i r) \bmod 256 \text{ where } C_i \text{ is a constant.}$$

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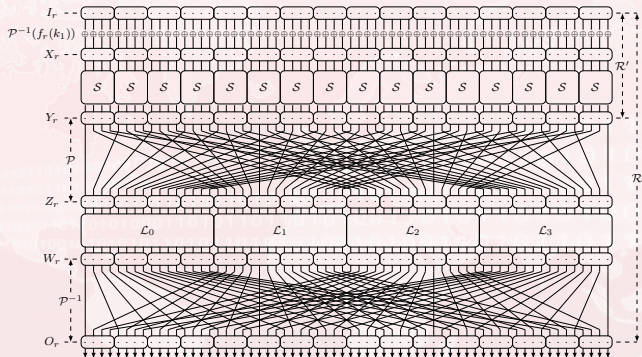
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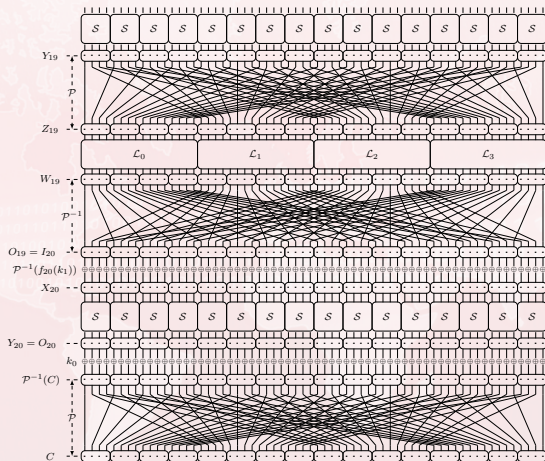
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### ■ The PRIDE round function



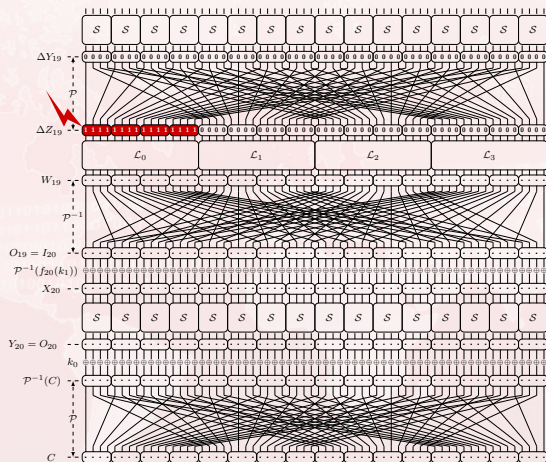
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### Injecting faults on $Z_{19}$

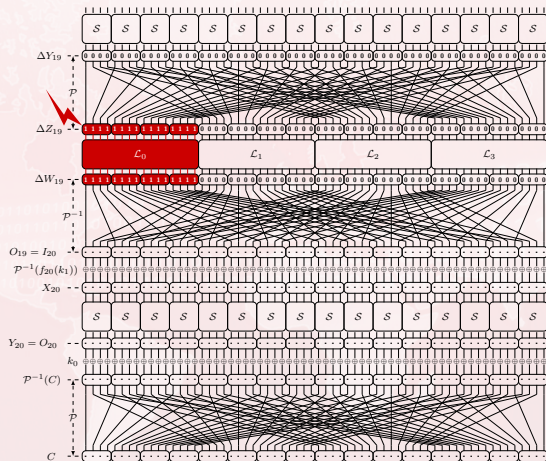




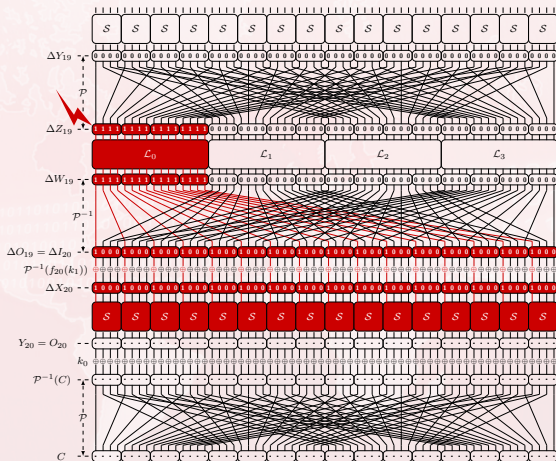
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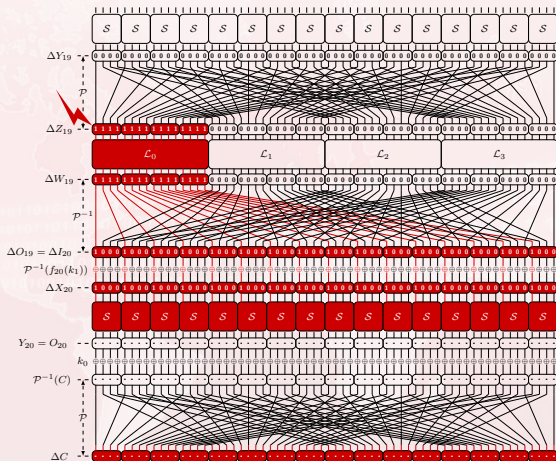
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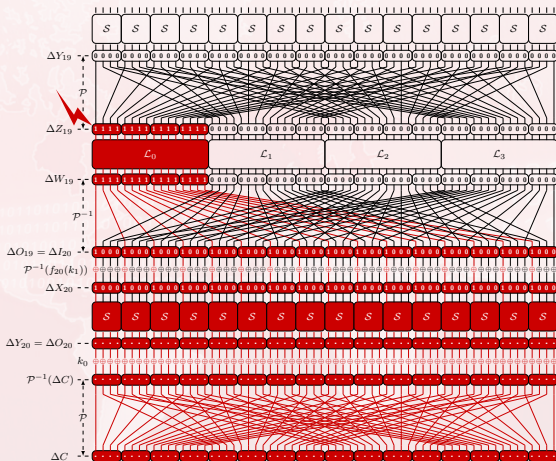
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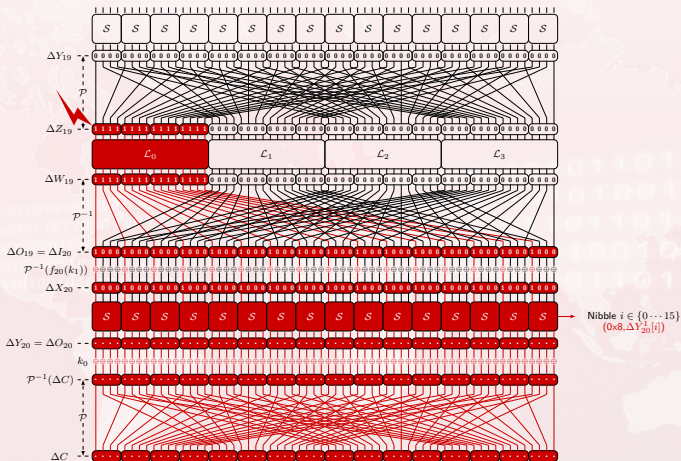
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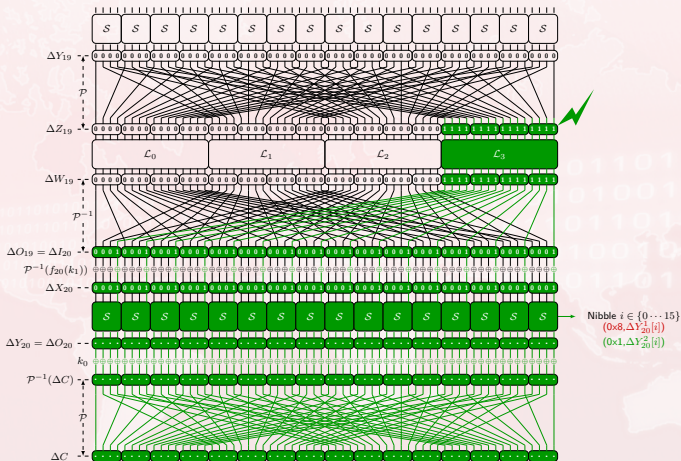
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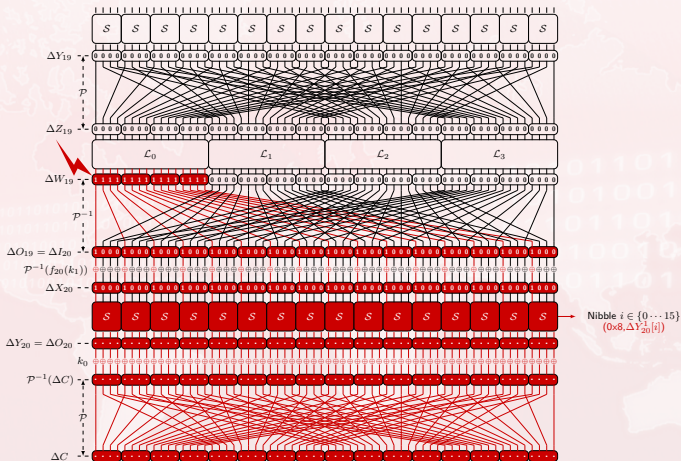
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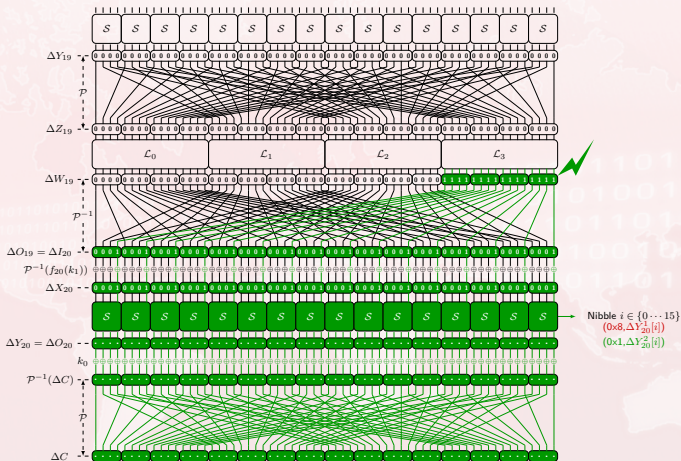


### Injecting faults on $W_{19}$





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### ■ Proposition

Let  $\mathcal{S}$  be an  $n$ -bit S-box with differential uniformity 4. Let  $(a_1, b_1)$  and  $(a_2, b_2)$  be two differentials with  $a_1 \neq a_2$  such that the system of two equations

$$\mathcal{S}(x \oplus a_1) \oplus \mathcal{S}(x) = b_1 \quad (1)$$

$$\mathcal{S}(x \oplus a_2) \oplus \mathcal{S}(x) = b_2 \quad (2)$$

has at least two solutions. Then, each of the three equations (1), (2) and

$$\mathcal{S}(x \oplus a_1 \oplus a_2) \oplus \mathcal{S}(x) = b_1 \oplus b_2 \quad (3)$$

has at least four solutions.

### Mathematical exploited relations

For all  $i$  in  $\{0, \dots, 15\}$

$$\Delta X_{20}[i] = \mathcal{S}^{-1}(\mathcal{P}^{-1}(C)[i] \oplus k_0[i]) \oplus \mathcal{S}^{-1}(\mathcal{P}^{-1}(C^*)[i] \oplus k_0[i])$$

Let

$$x = \mathcal{P}^{-1}(C)[i] \oplus k_0[i]$$

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### ■ Obtained differences

From injecting faults on  $Z_{19}$  or on  $W_{19}$

$$(a_1, 0x1), (a_2, 0x8)$$

Difference distribution table of the PRIDE S-box

T	0x0	0x1	0x2	0x3	0x4	0x5	0x6	0x7	0x8	0x9	0xa	0xb	0xc	0xd	0xe	0xf
0x0	16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x1	0	0	0	0	4	4	4	4	0	0	0	0	0	0	0	0
0x2	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
0x9	0	0	0	0	2	2	2	2	0	0	0	0	2	2	2	2
0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
0xe	0	0	2	2	0	0	2	2	0	2	0	2	2	0	2	0
0xf	0	0	2	2	2	2	0	0	0	2	0	2	0	2	0	2

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0x2	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
0x9	0	0	0	0	2	2	2	2	0	0	0	0	2	2	2	2
0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
0xe	0	0	2	2	0	0	2	2	0	2	0	2	2	0	2	0
0xf	0	0	2	2	2	2	0	0	0	2	0	2	0	2	0	2



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0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
0x9	0	0	0	0	2	2	2	2	0	0	0	0	2	2	2	2
0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
0xe	0	0	2	2	0	0	2	2	0	2	0	2	2	0	2	0
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0x1	0	0	0	0	4	4	4	4	0	0	0	0	0	0	0	0
0x2	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
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0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
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0x1	0	0	0	0	4	4	4	4	0	0	0	0	0	0	0	0
0x2	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
0x9	0	0	0	0	2	2	2	2	0	0	0	0	2	2	2	2
0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
0xe	0	0	2	2	0	0	2	2	0	2	0	2	2	0	2	0
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0x1	0	0	0	0	4	4	4	4	0	0	0	0	0	0	0	0
0x2	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x3	0	0	0	0	0	0	0	0	4	0	0	4	2	2	2	2
0x4	0	4	0	0	0	0	4	0	0	2	2	0	2	0	0	2
0x5	0	4	0	0	0	4	0	0	0	2	2	0	2	0	0	2
0x6	0	4	0	0	4	0	0	0	0	2	2	0	0	2	2	0
0x7	0	4	0	0	0	0	0	4	0	2	2	0	0	2	2	0
0x8	0	0	4	4	0	0	0	0	4	0	4	0	0	0	0	0
0x9	0	0	0	0	2	2	2	2	0	0	0	0	2	2	2	2
0xa	0	0	0	0	2	2	2	2	4	0	4	0	0	0	0	0
0xb	0	0	4	4	0	0	0	0	0	0	0	0	2	2	2	2
0xc	0	0	2	2	2	2	0	0	0	2	0	2	2	0	2	0
0xd	0	0	2	2	0	0	2	2	0	2	0	2	0	2	0	2
0xe	0	0	2	2	0	0	2	2	0	2	0	2	2	0	2	0
0xf	0	0	2	2	2	2	0	0	0	2	0	2	0	2	0	2

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- Flip the 16-bit output of one matrix after the  $\mathcal{L}$ -layer activates all S-boxes in the next round.

Use this property on the penultimate round allows the attacker to recover information on all nibbles of  $k_0$ .

The number of remaining candidates is at most  $4^{16}$ , where 4 is the differential-uniformity of the PRIDE S-box.

### The differential properties of the S-box

The number of inputs which satisfy two valid differentials simultaneously is usually reduced to a single element.

It is the case in each nibble for the presented strategies.



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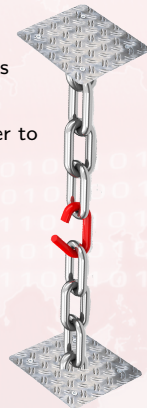
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- 1 The PRIDE block cipher
  - The structure of PRIDE
  - The PRIDE round function
- 2 Differential Fault Analysis of PRIDE
  - General principle
  - Differential properties of the PRIDE S-box
  - Properties that make the attack effective
- 3 **Practical implementation of the DFA on PRIDE**
  - Implementation of the device
  - Exploitation of obtained faults
- 4 Countermeasures
  - Duplication of computations
  - Desynchronization
  - Masking
- 5 Conclusion and perspectives

### ■ The chip used and our PRIDE implementation

- We have implemented PRIDE on a chip embedding an Cortex-M3 micro-controller. It is quite representative of the devices used for IoT applications.

In order to take advantage of the 32-bit architecture of the micro-controller, we have implemented PRIDE in ARM assembly language.

### The faults injection device

We used electromagnetic pulses to disrupt PRIDE execution. This approach requires no decapsulation of the chip and allows to precisely target a given time.

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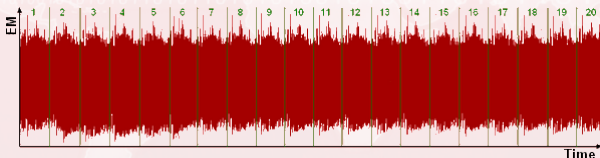
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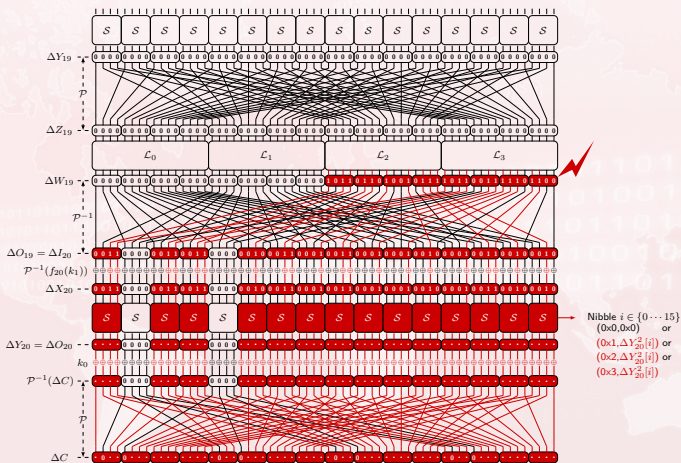
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### 32-bit random faults on $W_{19}$





### ■ The parameter values

- We used a key  $k = k_0 || k_1$  where

$k_0 = 0xf3f721cb1c882658$  and  $k_1 = 0xe417d148e239ca5d$

The plaintext used for all executions was  $0x0132546798badcfe$  and the correct ciphertext was  $0x9a ECB37ea45a6c89$ . We denote respectively by  $\theta, \beta, \gamma, \delta$  the possible pair of values  $(0x2, 0x3), (0x4, 0x8), (0x4, 0xc), (0x8, 0xc)$ .

### The obtained faults on the 19-th round

No.	Faulty ciphertext	Value of the fault on $W_{19}$	Value of $\Delta Y_{20}$	Value of $\Delta X_{20}$
1	0x1aad3b972c92ec09	0x00000000804108e8	0xf00060007e40600c	0x0000100010101000
2	0x7b4c93dea55a6d89	0x00000000e1a0a0a0	0x88c0000bc0c00000	0x0000000000000000
3	0x1b6c733e255aadcb	0x0000000081804040	0xf500000b85000000	0x0100000001000000
4	0x71ecd27ee55a6d89	0x00000000eb00e900	0x8ec0808f00000000	0x0000000000000000
5	0x9a ECB324a4426cdb	0x000000000000005a	0x000000005076050	0x000000001011010
6	0x9a57b33fa4626cf1	0x000000000bb005a	0x0000000085bbb08c	0x0000000001000000
7	0x9a57b365a4606cb9	0x000000000bb0000	0x0000000080bfe0ec	0x0000000000000000
8	0x77aa24313111ed8c	0x00000000ed461f4d	0xf8868e4f0e006de7	0x0001001000001001
9	0x9aeb8b37ac15a6989	0x6500040400000000	0x0220030300000c00	0x0000000000000000
10	0x8a ECB27e415abc89	0xe400d10000000000	0x3329020600000000	0x0000000000000000
11	0xa3e692ed909ee688	0x355fab9300000000	0x10ea921c620482c5	0x40c0000000000000
12	0x05ecb27e565a7289	0xf3001f0000000000	0xa22b99bc00000000	0x0000000000000000

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5	0x9aecb324a4426cdb	0x000000000000005a	0x000000005076050	0x000000001011010
6	0x9a57b33fa4626cf1	0x0000000000bb005a	0x0000000085bbb08c	0x0000000010000000
7	0x9a57b365a4606cb9	0x0000000000bb0000	0x0000000080bfe0ec	0x0000000000000000
8	0x77aa24313111ed8c	0x00000000ed461f4d	0xf8868e4f0e006de7	0x0001001000001001
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### Exploitation of the faults to retrieve $k_0$

No.	$k_0[0]$	$k_0[1]$	$k_0[2]$	$k_0[3]$	$k_0[4]$	$k_0[5]$	$k_0[6]$	$k_0[7]$	$k_0[8]$	$k_0[9]$	$k_0[10]$	$k_0[11]$	$k_0[12]$	$k_0[13]$	$k_0[14]$	$k_0[15]$
1	0x0 0x1 0xe 0xf	0	0	0	0x2 0x3 0x4 0x5	0	0	0	0x0 0x1 0x6 0x7	0x2 0x3 0xc 0xd	0x8 0x9 0xc 0xd	0	0x2 0x3 0x4 0x5	0	0	0x4 0x5 0x8 0x9
3	0x0 0x1 0xe 0xf	0x2 0x3 0x6 0x7	0	0	0	0	0	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x8 0x9 0xc 0xd	0	0	0	0	0	0
6	0	0	0	0	0	0	0	0	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x8 0x9 0xc 0xd	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0x4 0x5 0x8 0x9
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11	0xa 0xb 0xe 0xf	0	0x1 0xf	0x1 0x5 0x7 0xb 0xd 0xf	0x2 0x4 0xb 0xd	0x1 0x3 0x4 0x6 0x9 0xb	0x8 0x9 0xc 0xd 0xd	0x2 0x7 0x7 0xe	0x0 0x1 0x6 0x7	0x4 0x6 0x9 0xb 0xc 0xe	0	0x8 0xc	0x1 0x2 0x9 0xa	0x4 0x6 0x9 0xc 0xe	0x0 0x5 0xb 0xc	0x8 0xd
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$\cap$	0xf	0x3	0xf	0x7	0x2	0x1	0xc	0xb	0x0 0x1	0xc	0x8 0x9	0x8	0x2	0x6	0x5	0x8

- **Reducing the remaining candidates for  $k_0$  from faults obtained on the 18-th round**

- From the faulty ciphertext 0xf24690de8df8cc89 obtained from a fault on  $W_{18}$ , we obtain the 4 following values for  $\Delta Y_{19}$  for each possible value of  $k_0$

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f3f721cb0c882658	0xc000009022000000
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and since we know that we injected faults on the last 32 bits of  $W_{18}$ , we know that each nibble of  $\Delta X_{19}$  is either 0x0, 0x1, 0x2 or 0x3.

From the difference distribution table of the S-box, we see that an input difference equal to 0x1, 0x2 or 0x3 can lead to an output difference only in

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### ■ The obtained faults on the 18-th round

No.	Faulty ciphertext	Value of the fault on $W_{18}$	Value of $\Delta Y_{19}$	Value of $\Delta X_{19}$
13	0xf24690de8df8cc89	0x0000000082000000	0xc00000b000000000	0x0000000000000000
14	0x2df93aebf5935009	0x0000000041c0d0d0	0x7807000bd8050000	0x1001000000010000
15	0xa9a4a34f84604dde	0x0000000003010707	0x000004cd0000065c	0x0000010000000110
16	0x52c367c49a9b8786	0x0000000000b55858	0x05077000b6d84808	0x0101100001001000
17	0x00632c247f18e99e	0x00000000058580000	0x0e0bb0000d0ef000	0x0000000000000000
18	0xecbc98d50864ad3a	0x00000000a7a70000	0xc0f008bbb0d00888	0x0000000000000000
19	0x43b733ec34c1ec11	0x0093000000000000	0x00000000300a0022	0x0000000000000000
20	0xcabdf870ee423736	0x75e5575700000000	0x0c8c0b123baf049e	0x00780c4d8dc040c0c
21	0x46eb59132610ef55	0x01e0c60100000000	0x6f0001133aa00006	0x4400044d00000004
22	0x9d13b57cf2211618	0x13974cd400000000	0x0f036133290c0422	0x040d44d000000000
23	0x1247352b2400c0ed	0x0000006700000000	0x000000009900c96	0x0000000000000000
24	0x770a084c5528c599	0x6363000000000000	0x0a8000330aa00022	0x0080000000000000
25	0xc80ca16eb67b9711	0x3600a90000000000	0x6043623a00000000	0x40c04d0000000000

We first retrieved each nibble  $\text{Nib}_i$  of  $\mathcal{L}^{-1}(S(\mathcal{P}^{-1}(C) \oplus k_0) \oplus \mathcal{P}^{-1}(f_{20}(k_1)))$ .

### ■ Exploitation of the faults to retrieve $\mathcal{L}^{-1}(S(\mathcal{P}^{-1}(C) \oplus k_0) \oplus \mathcal{P}^{-1}(f_{20}(k_1)))$

No.	Nib <sub>0</sub>	Nib <sub>1</sub>	Nib <sub>2</sub>	Nib <sub>3</sub>	Nib <sub>4</sub>	Nib <sub>5</sub>	Nib <sub>6</sub>	Nib <sub>7</sub>	Nib <sub>8</sub>	Nib <sub>9</sub>	Nib <sub>10</sub>	Nib <sub>11</sub>	Nib <sub>12</sub>	Nib <sub>13</sub>	Nib <sub>14</sub>	Nib <sub>15</sub>
16	0	0x2 0x3 0x6 0x7	0	0x8 0x9 0xe 0xf	0x8 0x9 0xe 0xf	0	0	0	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0xa 0xb 0xa 0xb	0x6 0x7 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x4 0x5	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb
17	0	0x2 0x3 0xa 0xb	0	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0	0	0	0	0x6 0x7 0xa 0xb	0	0x2 0x3 0xa 0xb	0x0 0x1 0xe 0xf	0	0	0
18	0x4 0x5 0x8 0x9	0	0x0 0x1 0xe 0xf	0	0	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0x4 0x5 0x6 0x7 0xc 0xd 0xe 0xf	0	0x6 0x7 0xa 0xb	0	0	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb	0x0 0x1 0x2 0x3 0x8 0x9 0xa 0xb
20	0	0x3 0x6 0xa 0xf	0x5 0x6 0xd 0xe	0x3 0x6 0xa 0xf	0	0x0 0xb	0x0 0x1 0x4 0x5	0x0 0x2 0x5 0x8 0xa	0x1 0x2 0x4 0x7 0xc 0xf	0	0x1 0x3 0x7 0x9 0xb 0xd	0	0	0xa 0xe	0x2 0x4 0xb 0xd	0x6 0x8
22	0	0x3 0xc	0	0x1 0x2 0x4 0x7 0xc 0xf	0x8 0x9 0xe 0xf	0x0 0x1 0x4 0x5	0x1 0x2 0x4 0x7 0xc 0xf	0x1 0x2 0x4 0x7 0xc 0xf	0x0 0x2 0x5 0x7 0xa	0x2 0x4 0xb 0xd	0	0x3 0x6 0xa 0xf	0	0xa 0xe	0x0 0x2 0x5 0x7 0x8 0xa	0x0 0x2 0x5 0x7 0x8 0xa
23	0	0	0	0	0	0	0	0	0	0x2 0x4 0xb 0xd	0x2 0x4 0xb 0xd	0	0	0x3 0x6 0xa 0xf	0x2 0x4 0xb 0xd	0x8 0x9 0xe 0xf
25	0x8 0x9 0xe 0xf	0	0xa 0xe	0x1 0x4 0x7 0xc 0xf	0x8 0x9 0xe 0xf	0x0 0x2 0x5 0x7 0x8 0xa	0x1 0x2 0x4 0x7 0xc 0xf	0x1 0x3 0x7 0x9 0xb 0xd	0	0	0	0	0	0	0	0
∩	0x8 0x9	0x3	0xe	0xf	0xe 0xf	0x0	0x4	0x7	0x7	0xb	0xb	0x3	0x0 0x1	0xa	0x2	0x8

### ■ Calculating the value of $k$

- By intersecting sets for each nibble, we got 8 candidates for

$$\mathcal{L}^{-1}(S(\mathcal{P}^{-1}(C) \oplus k_0) \oplus \mathcal{P}^{-1}(f_{20}(k_1)))$$

Then, we calculated the 8 possible  $S(\mathcal{P}^{-1}(C) \oplus k_0) \oplus \mathcal{P}^{-1}(f_{20}(k_1))$ , and from

$$S(\mathcal{P}^{-1}(C) \oplus k_0) = 0x128bb20f824eda39,$$

we deduced 8 candidates for  $\mathcal{P}^{-1}(f_{20}(k_1))$ .

Finally we got 8 values for  $f_{20}(k_1)$  and so for  $k_1$ .

We eventually obtained, by testing all possible  $k_1$ , the secret key

$$k = 0xf3f721cb1c882658e417d148e239ca5d$$

from a few number of faults.

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- 5 Conclusion and perspectives

### Description

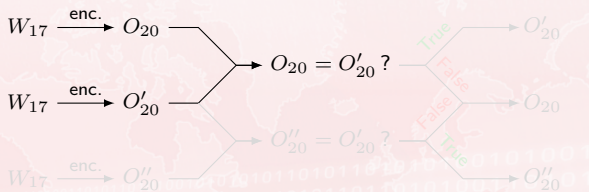


### Cost per duplication

- 2 matrix layers
  - 3 substitution layers
  - 3 subkey updates
  - 3 subkey additions
- Total < 15% of PRIDE enc./dec.



### Description

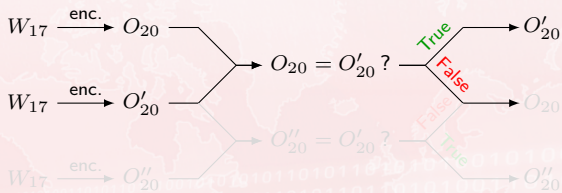


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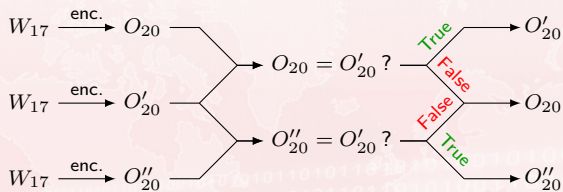


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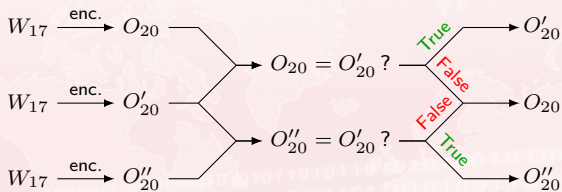
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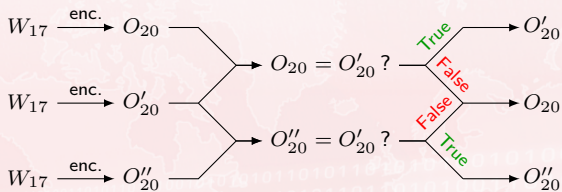
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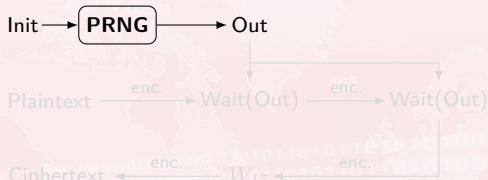


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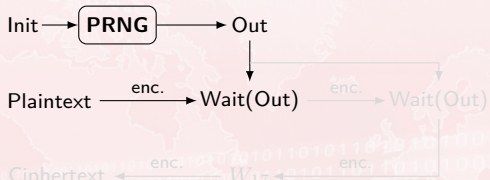
Generation of the PRNG's output

Access to the PRNG's output

Duration of the 'random delay'



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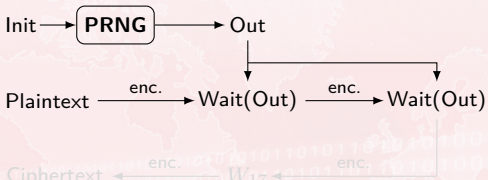
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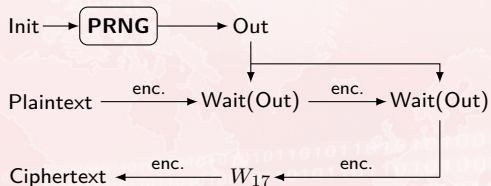
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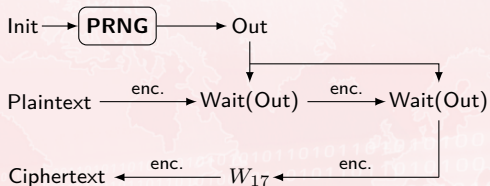
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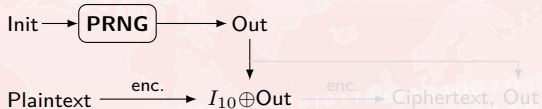
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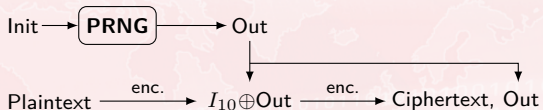
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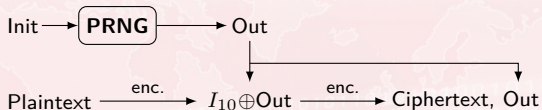
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- First DFA on PRIDE with 4 faults only to retrieve the full secret key.

Practical implementation from 32-bit random faults obtained with electromagnetic injection, which is a low-cost means of injection.

Resistance against DFA is important for a cipher like PRIDE, which will be dedicated to low-end devices thanks to its lightness.

Some countermeasures which leave the cipher still efficient for IoT devices.

## Perspectives

Optimize countermeasures to make them less costly and keep the light side of PRIDE : be careful that the protections do not open doors to further attacks.

Apply our attack to SPN-based block ciphers with a linear layer similar to the one used in PRIDE like the LS-Designs family : will be studied in a future work.

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THANKS FOR YOUR ATTENTION

Commissariat à l'énergie atomique et aux énergies alternatives

Benjamin Lac | DRT/CEATech/DPACA/LSAS

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